



VOLTAGE DETECTOR

FEATURES

- Single Voltage Detector (TPS3803): Adjustable/1.5 V
- Dual Voltage Detector (TPS3805): Adjustable/3.3 V
- High ±1.5% Threshold Voltage Accuracy
- Supply Current: 3 μA Typical at V_{DD} = 3.3 V
- Push/Pull Reset Output (TPS3805) Open-Drain Reset Output (TPS3803)
- Temperature Range: -40°C to +85°C
- 5-Pin SC-70 Package

DESCRIPTION

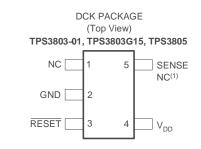
The TPS3803 and TPS3805 families of supervisory circuits provide circuit initialization and timing supervision, primarily for DSPs and processor-based systems.

The TPS3803G15 device has a fixed-sense threshold voltage $V_{\rm IT}$ set by an internal voltage divider, whereas the TPS3803–01 has an adjustable SENSE input that can be configured by two external resistors. In addition to the fixed sense threshold monitored at $V_{\rm DD}$, the TPS3805 devices provide a second adjustable SENSE input. RESET is asserted in case any of the two voltages drops below $V_{\rm IT}$.

During power on, RESET is asserted when supply voltage V_{DD} becomes higher than 0.8 V. Thereafter, the supervisory circuit monitors V_{DD} (and/or SENSE) and keeps RESET active as long as V_{DD} or SENSE remains below the threshold voltage V_{IT} . As soon as V_{DD} (SENSE) rises above the threshold voltage V_{IT} , RESET is deasserted again. The product spectrum is designed for 1.5 V, 3.3 V, and adjustable supply voltages. The devices are available in a 5-pin SC-70 package. The TPS3803 and TPS3805 devices are characterized for operation over a temperature range of -40°C to $+85^{\circ}\text{C}$.

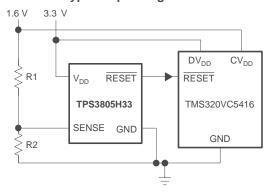
APPLICATIONS

- Applications Using DSPs, Microcontrollers, or Microprocessors
- Wireless Communication Systems
- Portable/Battery-Powered Equipment
- Programmable Controls
- Intelligent Instruments
- Industrial Equipment
- Notebook/Desktop Computers
- Automotive Systems



(1) NC = No Connection on TPS3803G15

Typical Operating Circuit





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.











This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

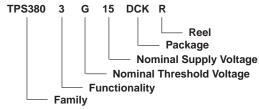
ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

PACKAGE INFORMATION

_	DEVICE MAME	THRESHOL	MARKING	
TA	DEVICE NAME	V_{DD}	SENSE	MARKING
	TPS3803-01DCKR ⁽¹⁾	NA	1.226 V	AWG
-40°C to +85°C	TPS3803G15DCKR(1)	1.40 V	NA	AWI
	TPS3805H33DCKR ⁽¹⁾	3.05 V	1.226 V	AWK

⁽¹⁾ The DCKR passive indicates tape and reel containing 3000 parts.

ORDERING INFORMATION



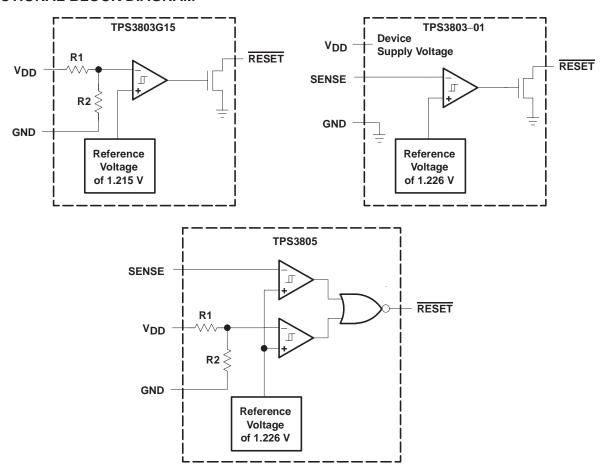
Function/Truth Tables

TPS3803-	01	TPS3803G15		
SENSE > VIT	RESET	V _{DD} > V _{IT}	RESET	
0	L	0	L	
1	Н	1	Н	

TPS3805H33						
V _{DD} > V _{IT}	RESET					
0	0	L				
0	1	L				
1	0	L				
1	1	Н				

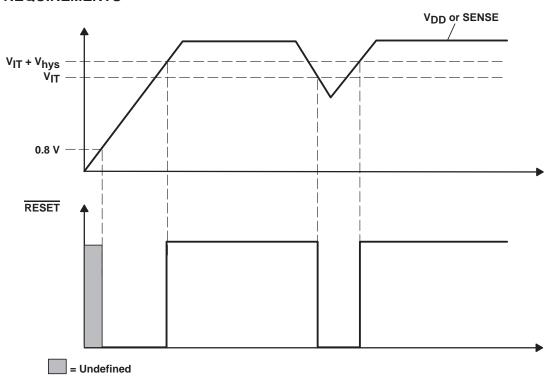


FUNCTIONAL BLOCK DIAGRAM



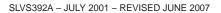


TIMING REQUIREMENTS



Terminal Functions

TERMINAL	TERMINAL		RMINAL I/O		DESCRIPTION		
NAME	NO.	1/0	DESCRIPTION				
GND	2	- 1	Ground				
RESET	3	0	Active-low reset output (TPS3803—open-drain, TPS3805—push/pull)				
SENSE	5	-1	Adjustable sense input				
NC	1		No internal connection				
NC (TPS3803G15)	5		No internal connection				
V_{DD}	4	I	Input supply voltage, fixed sense input for TPS3803G15 and TPS3805				





ABSOLUTE MAXIMUM RATINGS(1)

Over operating free-air temperature range, unless otherwise noted.

Supply voltage, V _{DD} ⁽²⁾	+7 V
All other pins ⁽²⁾	
Maximum low-output current, I _{OL}	+5 mA
Maximum high-output current, I _{OH}	
Input clamp current, I _{IK} (V _I < 0 or V _I > V _{DD})	
Output clamp current, I_{OK} ($V_O < 0$ or $V_O > V_{DD}$)	±10 mA
Continuous total power dissipation	See Dissipation Rating Table
Operating free-air temperature range, T _A	–40°C to +85°C
Storage temperature range, T _{stq}	–65°C to +150°C
Soldering temperature	

⁽¹⁾ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute—maximum—rated conditions for extended periods may affect device reliability.

DISSIPATION RATING TABLE

PACKAGE	T _A < +25°C	DERATING FACTOR	T _A = +70°C	T _A = +85°C
	POWER RATING	ABOVE T _A = +25°C	POWER RATING	POWER RATING
DCK	321 mW	2.6 mW/°C	206 mW	167 mW

RECOMMENDED OPERATING CONDITIONS

	MIN	MAX	UNIT
Supply voltage, V _{DD}	1.3	6	V
Input voltage, V _I	0	V _{DD} + 0.3	V
Operating free-air temperature range, T _A	-40	85	°C

⁽²⁾ All voltage values are with respect to GND. For reliable operation the device should not be continuously operated at 7 V for more than t = 1000 h.



ELECTRICAL CHARACTERISTICS

Over recommended operating free-air temperature range, unless otherwise noted.

	PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT	
Vон	High-level output voltage (TPS3805 on	$V_{DD} = 1.5 \text{ V}, I_{OH} = -0.5 \text{ mA}$ $V_{DD} = 3.3 \text{ V}, I_{OH} = -1.0 \text{ mA}$ $V_{DD} = 6 \text{ V}, I_{OH} = -1.5 \text{ mA}$	0.8 x V _{DD}			V		
VOL	Low-level output voltage	$V_{DD} = 1.5 \text{ V}, I_{OL} = 1.0 \text{ mA}$ $V_{DD} = 3.3 \text{ V}, I_{OL} = 2 \text{ mA}$ $V_{DD} = 6 \text{ V}, I_{OL} = 3 \text{ mA}$			0.3	V		
	Power up recet voltage(1)	VIT > 1.5 V, TA = 1	25°C	0.8			V	
	Power-up reset voltage ⁽¹⁾	$VIT \le 1.5 \text{ V}, \text{ TA} = 3$	25°C	1.0			V	
		SENSE		1.208	1.226	1.244		
VIT	Negative-going input threshold voltage(2)	TPS3803G15	$T_A = -40^{\circ}C$ to $+85^{\circ}C$	1.379	1.4	1.421	V	
	Vollago	TPS3805H33		3.004	3.05	3.096		
.,	Thursday and a		1.2 V < V _{IT} < 2.5 V		15		>/	
V _{hys}	Hysteresis		2.5 V < V _{IT} < 3.5 V		30		mV	
lį	Input current	SENSE		-25		25	nA	
lOH	High-level output current at RESET	Open drain only	$V_{DD} = V_{IT} + 0.2V$, $V_{OH} = V_{DD}$			300	nA	
		TPS3803-01			2	4		
		TPS3805, TPS3803G15	V _{DD} = 3.3 V, output unconnected		3	5		
IDD	Supply current	TPS3803-01			2	4	μΑ	
		TPS3805, TPS3803G15	V _{DD} = 6 V, output unconnected		4	6		
Cl	Input capacitance		$V_I = 0 V \text{ to } V_{DD}$		1		pF	

TIMING REQUIREMENTS

AT R_L = 1 M Ω , C_L = 50 PF, T_A = -40° C TO +85 $^{\circ}$ C.

	PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
	Dules wielth	At V _{DD}	V 4.05 x V V 0.05 x V				
τ _W	Pulse width	At SENSE	$V_{IH} = 1.05 \times V_{IT}, V_{IL} = 0.95 \times V_{IT}$	5.5			μs

SWITCHING CHARACTERISTICS

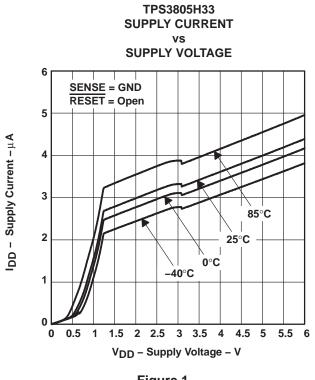
AT R_L = 1 M Ω , C_L = 50 PF, T_A = -40° C TO +85 $^{\circ}$ C.

PARAMETER			TEST CONDITIONS	MIN	TYP	MAX	UNIT
tPHL	Propagation (delay) time, high-to-low-level output		V _{IH} = 1.05 x V _{IT} ,		5	100	
	Propagation (delay) time,	V _{DD} to RESET delay	V _{IL} = 0.95 x V _{IT}			400	μs
^t PLH	low-to-high-level output	SENSE to RESET delay			5	100	

⁽¹⁾ The lowest supply voltage at which \overline{RESET} (V_{OL}(max) = 0.2 V, I_{OL} = 50 μ A) becomes active. $t_{\Gamma}(VDD) \ge 15 \,\mu$ s/V. (2) To ensure the best stability of the threshold voltage, place a bypass capacitor (ceramic, 0.1 μ F) near the supply terminals.



TYPICAL CHARACTERISTICS



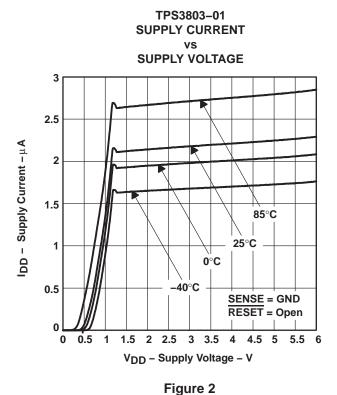
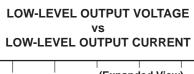


Figure 1



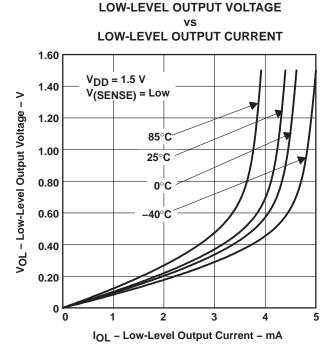


Figure 3

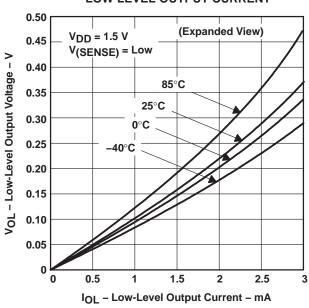


Figure 4



TYPICAL CHARACTERISTICS (continued)

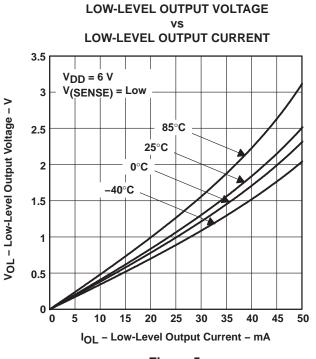


Figure 5

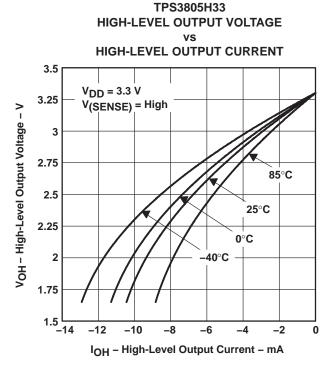


Figure 7

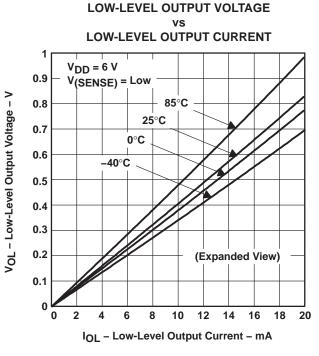


Figure 6

TPS3805H33

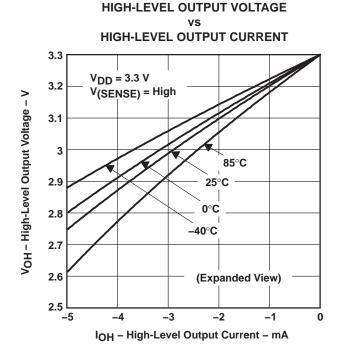
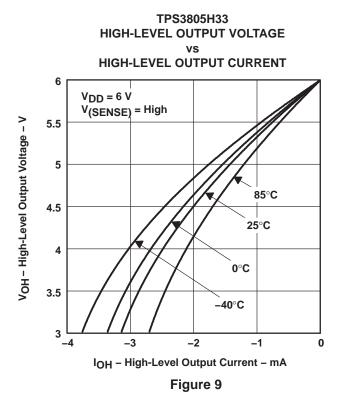


Figure 8

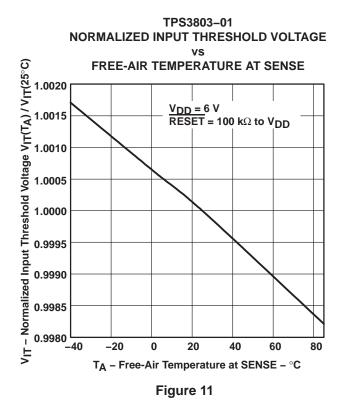
TPS3805H33

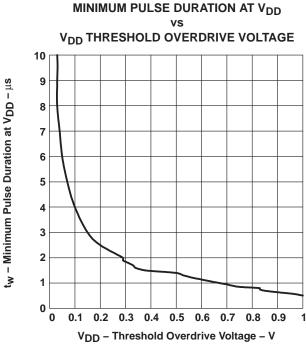


TYPICAL CHARACTERISTICS (continued)



HIGH-LEVEL OUTPUT VOLTAGE ٧S **HIGH-LEVEL OUTPUT CURRENT** 6 $V_{DD} = 6 V$ 5.8 V(SENSE) = High VOH - High-Level Output Voltage - V 5.6 5.4 25°C 5.2 0°C -40°C 5 (Expanded View) 4.8 -10 IOH - High-Level Output Current - mA Figure 10







TYPICAL CHARACTERISTICS (continued)

MINIMUM PULSE DURATION AT SENSE VS SENSE THRESHOLD OVERDRIVE VOLTAGE

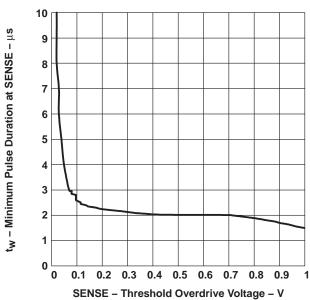


Figure 13

Revision History

DATE	REV	PAGE	SECTION	DESCRIPTION		
6/07	Δ	Front Page	_	Updated front page.		
0/01			_	Functional block diagram change.		

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.





18-Sep-2008

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TPS3803-01DCKR	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3803-01DCKRG4	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3803G15DCKR	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3803G15DCKRG4	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3805H33DCKR	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3805H33DCKRG4	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): Ti's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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OTHER QUALIFIED VERSIONS OF TPS3803-01, TPS3803G15, TPS3805H33:

- Automotive: TPS3803-01-Q1, TPS3803G15-Q1, TPS3805H33-Q1
 Enhanced Product: TPS3803-01-EP, TPS3803G15-EP, TPS3805H33-EP

NOTE: Qualified Version Definitions:

- Automotive Q100 devices qualified for high-reliability automotive applications targeting zero defects
- Enhanced Product Supports Defense, Aerospace and Medical Applications

PACKAGE MATERIALS INFORMATION

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TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

TAPE AND REEL INFORMATION

*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS3803-01DCKR	SC70	DCK	5	3000	178.0	9.0	2.4	2.5	1.2	4.0	8.0	Q3
TPS3803-01DCKR	SC70	DCK	5	3000	180.0	8.4	2.25	2.4	1.22	4.0	8.0	Q3
TPS3803G15DCKR	SC70	DCK	5	3000	180.0	8.4	2.25	2.4	1.22	4.0	8.0	Q3
TPS3805H33DCKR	SC70	DCK	5	3000	178.0	9.0	2.4	2.5	1.2	4.0	8.0	Q3
TPS3805H33DCKR	SC70	DCK	5	3000	180.0	8.4	2.25	2.4	1.22	4.0	8.0	Q3

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*All dimensions are nominal

All differentiations are normal										
Device	Package Type	Package Drawing	Pins SPQ		Length (mm)	Width (mm)	Height (mm)			
TPS3803-01DCKR	SC70	DCK	5	3000	180.0	180.0	18.0			
TPS3803-01DCKR	SC70	DCK	5	3000	202.0	201.0	28.0			
TPS3803G15DCKR	SC70	DCK	5	3000	202.0	201.0	28.0			
TPS3805H33DCKR	SC70	DCK	5	3000	180.0	180.0	18.0			
TPS3805H33DCKR	SC70	DCK	5	3000	202.0	201.0	28.0			

DCK (R-PDSO-G5)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
- D. Falls within JEDEC MO-203 variation AA.



DCK (R-PDSO-G5)

PLASTIC SMALL OUTLINE



NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
- D. Publication IPC-7351 is recommended for alternate designs.
- E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.



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