

TPS65910 Silicon Errata ES1.1

This document describes the TPS65910 bugs, limitations, and enhancements with suggested workarounds. As this information is dated, updates will be provided.

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1 Glitch on SDA-SCL not managed correctly by the I²C IP

Impact:

The standard specifies that the I²C transfer should restart on a start event in all cases. The current design does not support two consecutive Start conditions. This can cause the first real access after such a glitch to be corrupted.

Description:

An unexpected glitch on SDA and SCL can generate a wrong start event. In the current design, the SCL line must toggle two times to detect a new start event and completely restart the I²C access; hence the real start event is not detected in the case of a single SCL toggle.

Workaround:

Repeat I²C access.

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