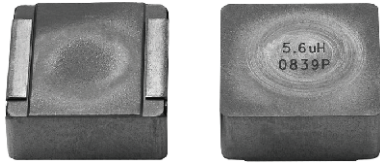


## Low Profile, High Current IHLP® Inductors



Manufactured under one or more of the following:  
**US Patents; 6,198,375/6,204,744/6,449,829/6,460,244.**  
 Several foreign patents, and other patents pending.



**RoHS**  
 COMPLIANT  
 HALOGEN  
**FREE**

### FEATURES

- Shielded construction
- Excellent DC/DC energy storage up to 1 MHz to 2 MHz filter inductor applications up SRF (see Standard Electrical Specifications table)
- Lowest DCR/ $\mu$ H, in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- Material categorization: For definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

### APPLICATIONS

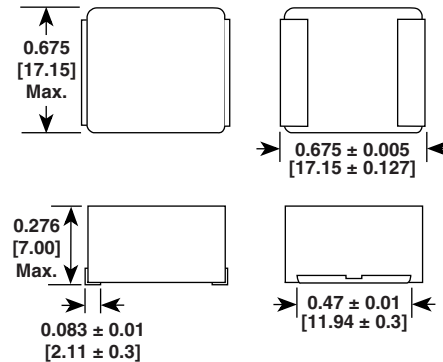
- PDA/notebook/desktop/server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)

STANDARD ELECTRICAL SPECIFICATIONS					
$L_0$ INDUCTANCE $\pm 20\%$ AT 100 kHz, 0.25 V, 0 A ( $\mu$ H)	DCR TYP. 25 °C (m $\Omega$ )	DCR MAX. 25 °C (m $\Omega$ )	HEAT RATING CURRENT DC TYP. (A) <sup>(3)</sup>	SATURATION CURRENT DC TYP. (A) <sup>(4)</sup>	SFR TYP. (MHz)
0.33	0.61	0.67	75.5	55	78
0.47	0.78	0.87	72	57	60
0.56	0.83	0.91	61	66	40
0.82	0.98	1.08	56.5	45	36
1.0	1.21	1.27	55.5	32	34
1.5	1.54	1.62	48	31	26
2.2	1.85	1.98	43.5	28	19
3.3	2.79	2.93	35	27	16
4.7	3.98	4.18	30	21	10.7
5.6	4.23	4.44	28	21	11.8
6.8	5.86	6.15	22.5	18.5	10.0
8.2	7.71	8.10	21	18	10.0
10.0	8.89	9.33	19	17	8.0
15.0	13.7	14.4	14	12	7.5
22.0	20.0	21.0	12	9.5	4.3
33.0	35.1	37.0	10.7	9	4.8
47.0	40.7	42.7	8.7	8.6	4.1
56.0	55	57.8	7.2	4.2	2.9
68.0	72.1	75.7	6.1	4.5	3.0
82.0	87.3	91.7	5.5	4.5	2.6
100.0	105	110	5.0	4.0	2.1

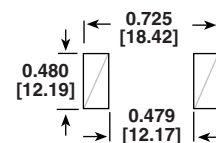
### Notes

- (1) All test data is referenced to 25 °C ambient
- (2) Operating temperature range - 55 °C to + 125 °C
- (3) DC current (A) that will cause an approximate  $\Delta T$  of 40 °C
- (4) DC current (A) that will cause  $L_0$  to drop approximately 20 %
- (5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

### DIMENSIONS in inches [millimeters]



### Typical Pad Layout

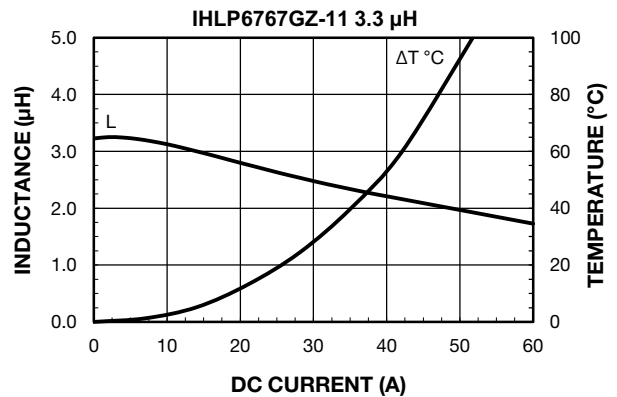
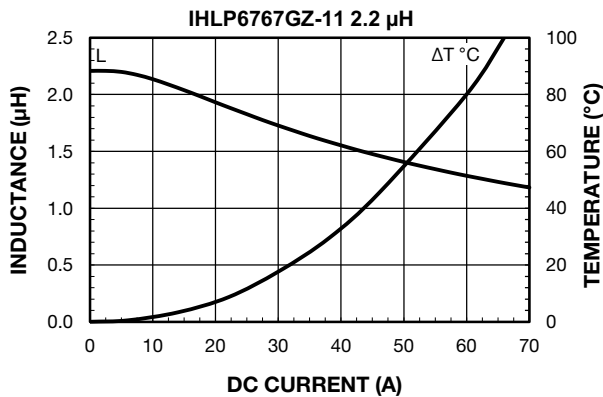
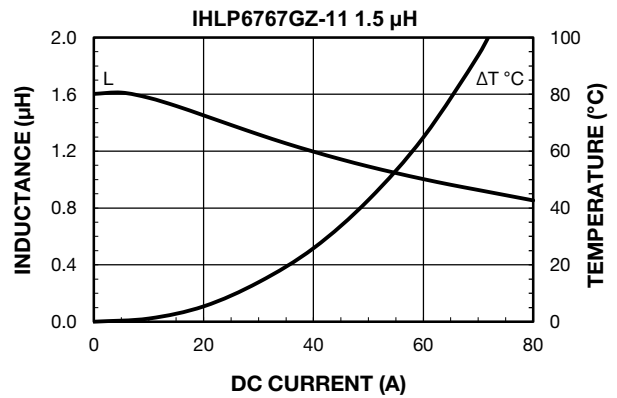
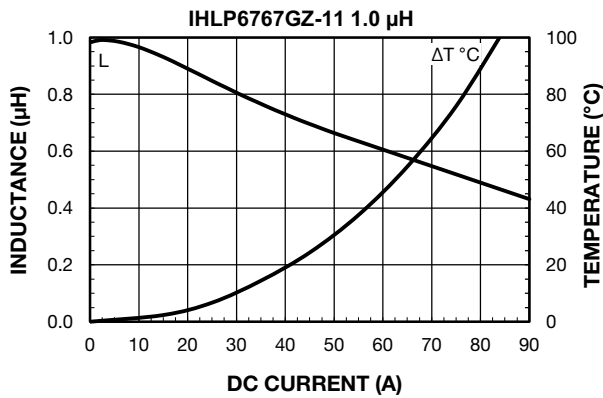
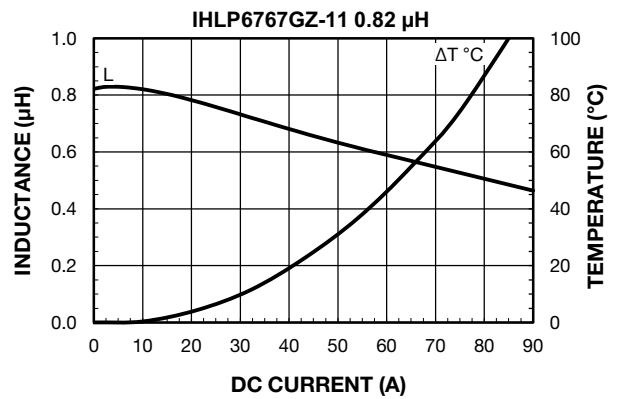
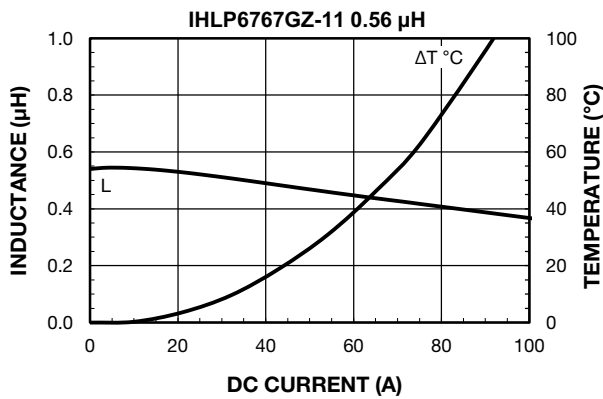
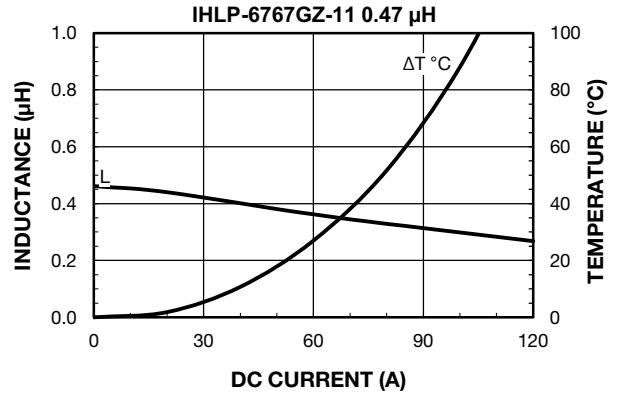
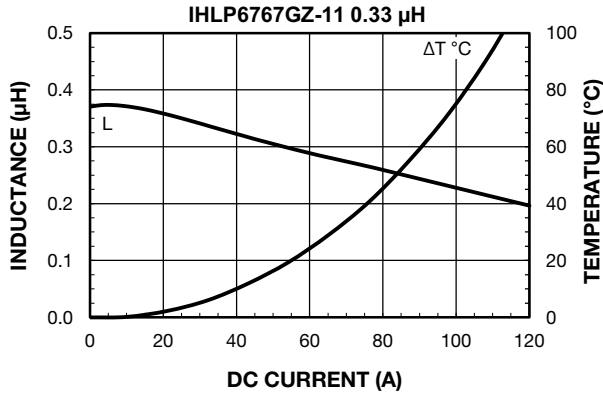


DESCRIPTION					
IHLP-6767GZ-11	4.7 $\mu$ H	$\pm 20\%$	ER	e3	
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD	

GLOBAL PART NUMBER																	
I	H	L	P	6	7	6	7	G	Z	E	R	4	R	7	M	1	1
PRODUCT FAMILY				SIZE				PACKAGE CODE		INDUCTANCE VALUE			TOL.	SERIES			

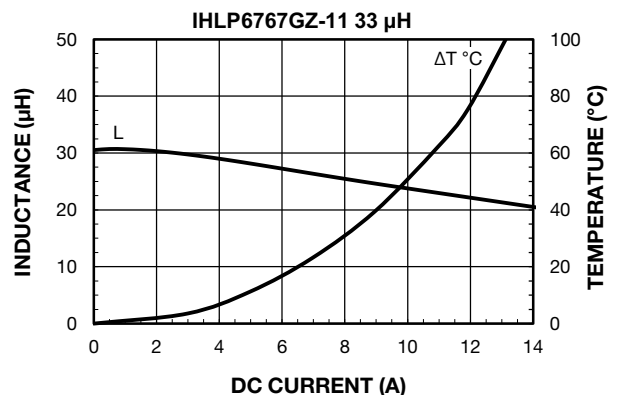
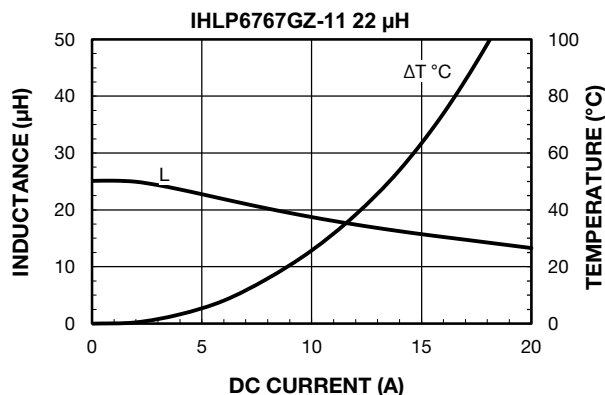
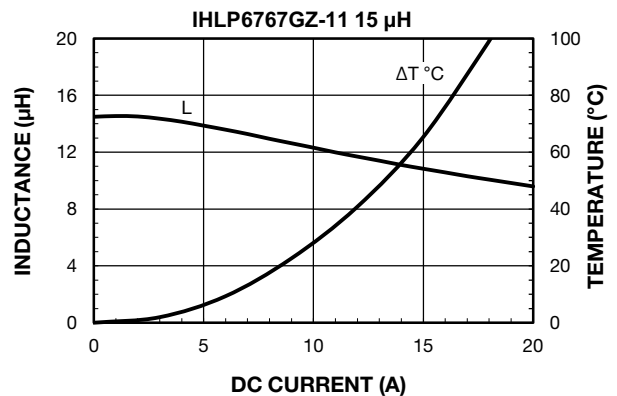
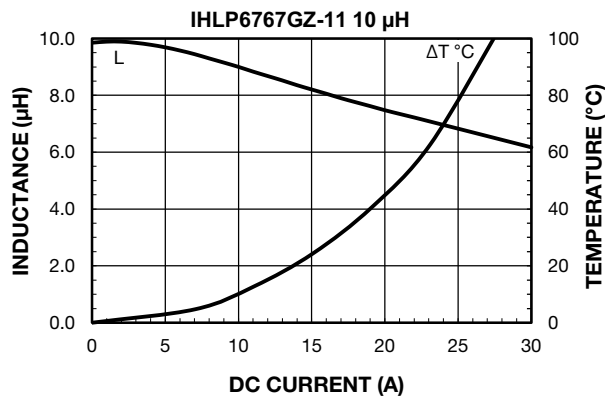
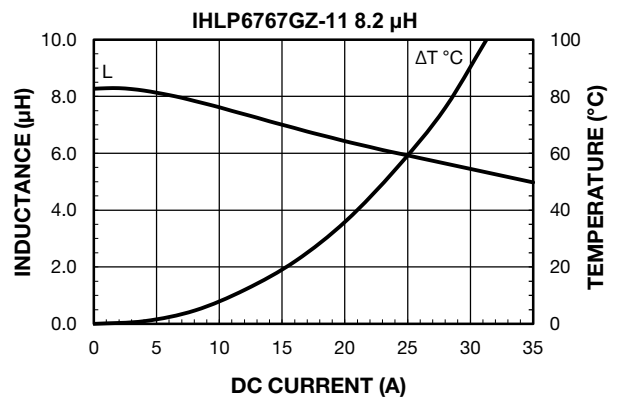
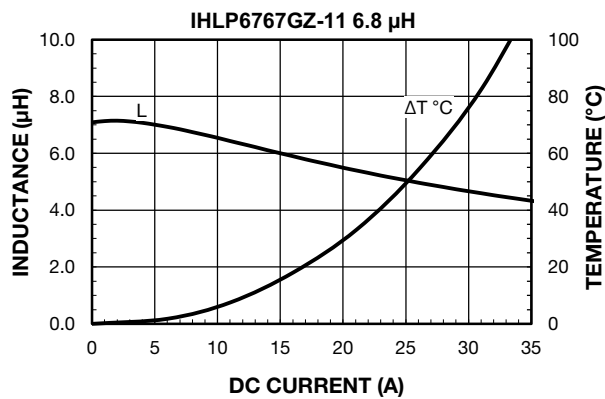
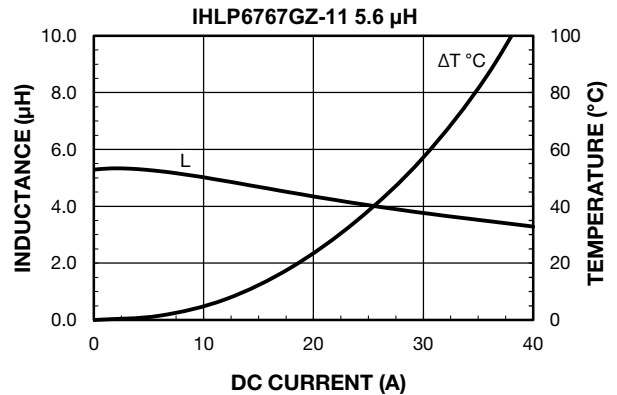
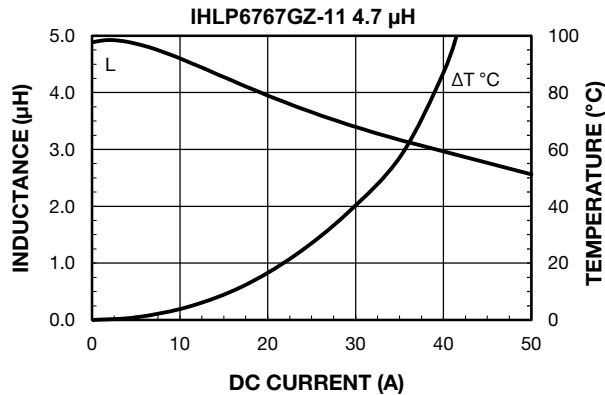


PERFORMANCE GRAPHS



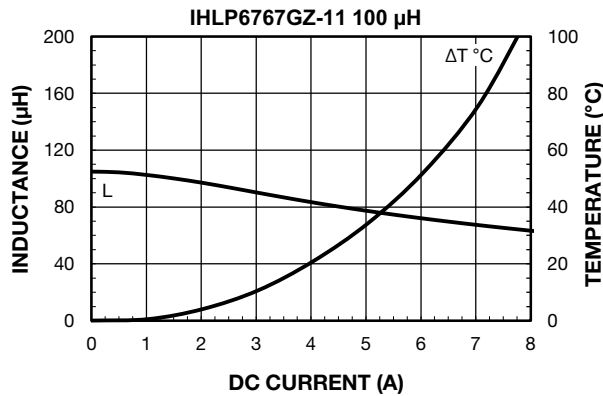
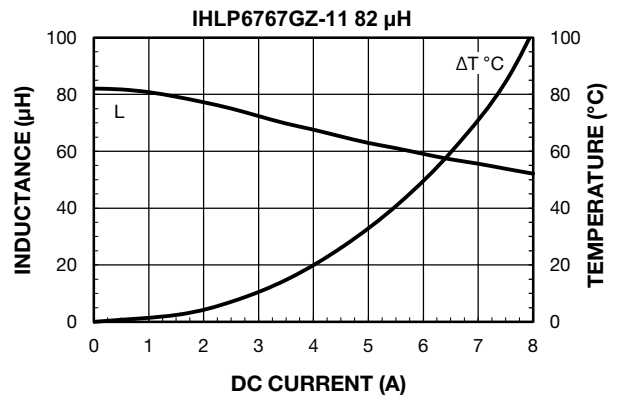
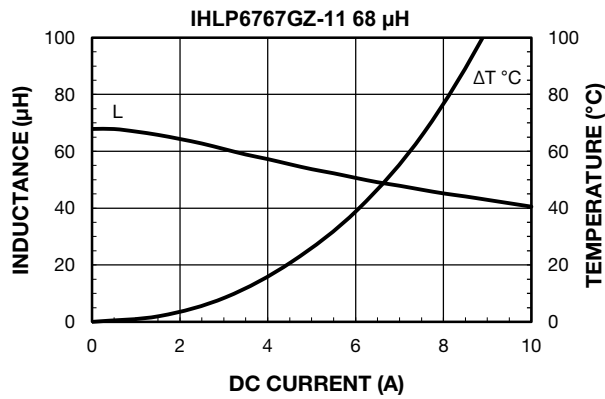
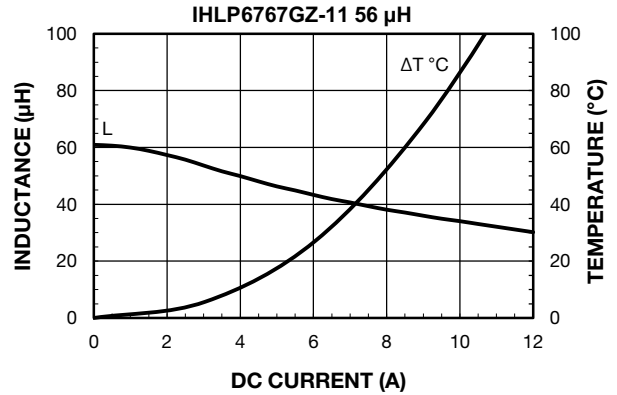
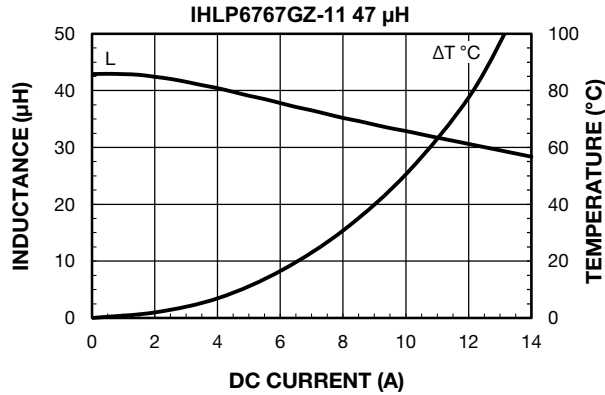


PERFORMANCE GRAPHS

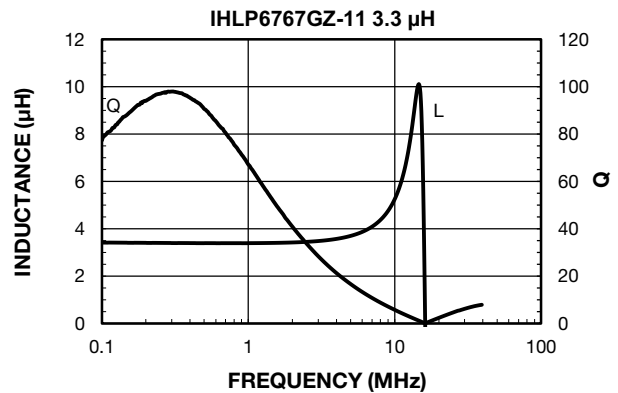
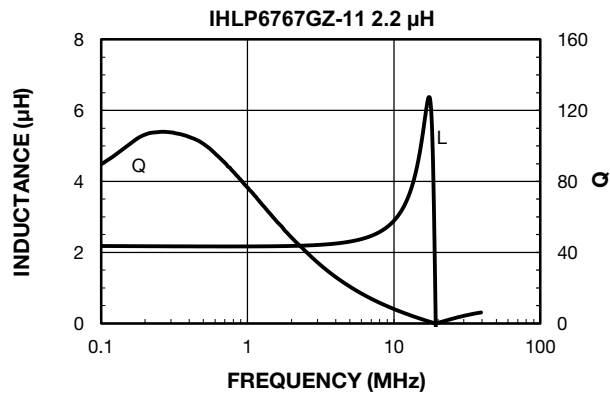
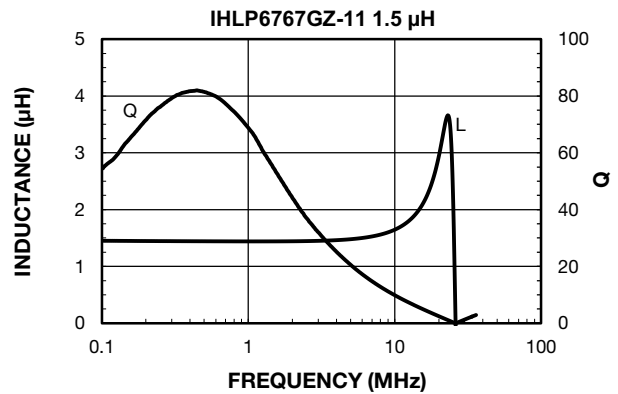
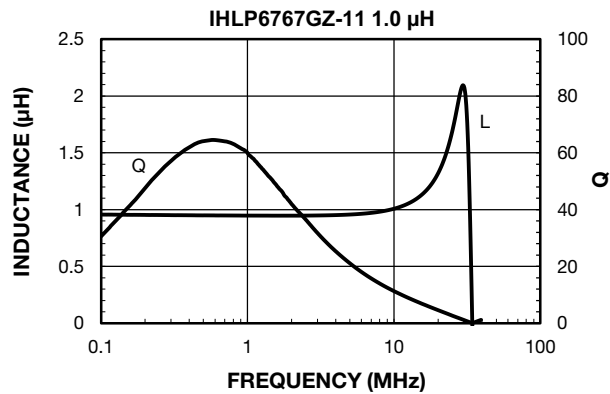
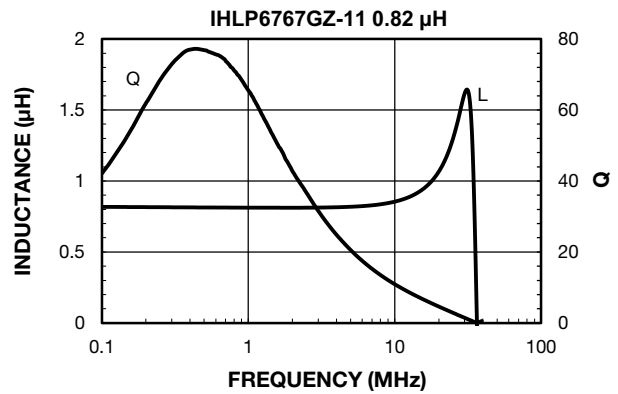
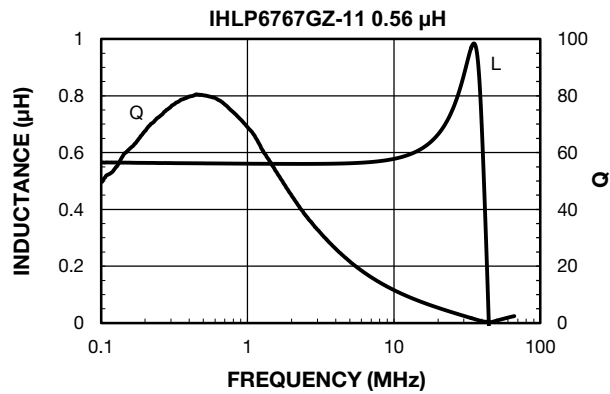
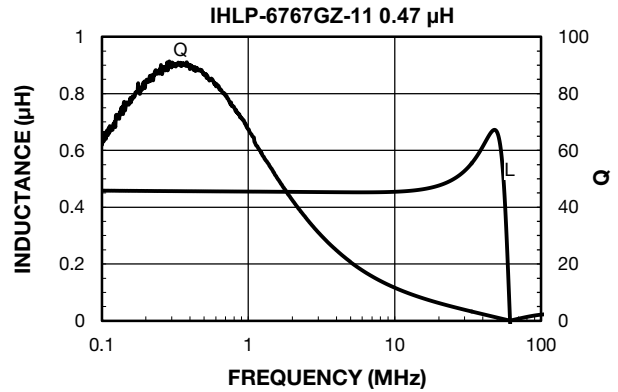
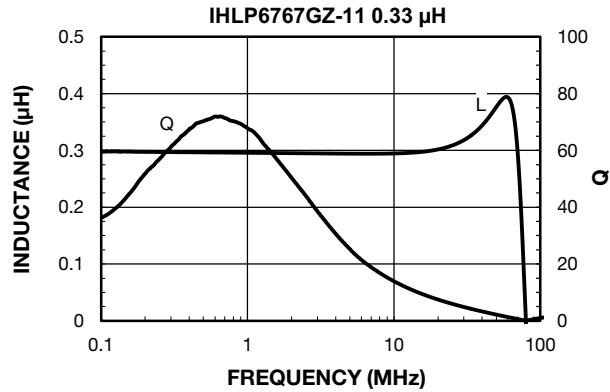




PERFORMANCE GRAPHS

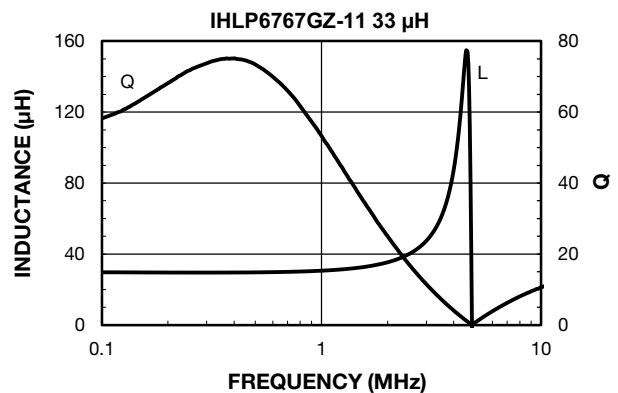
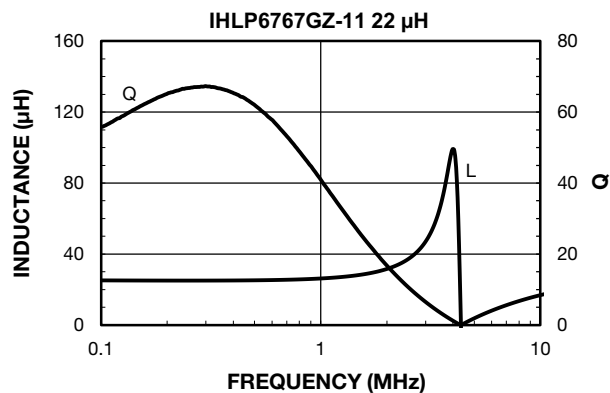
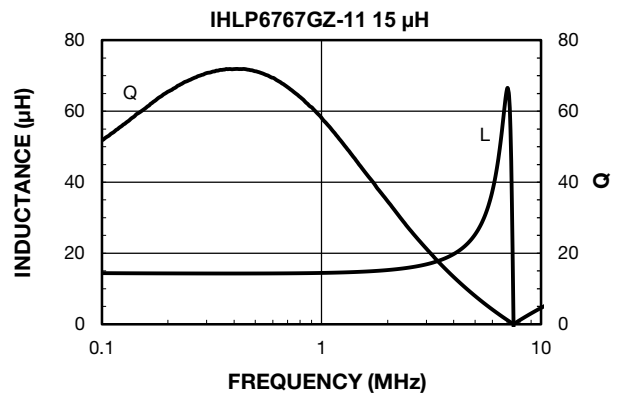
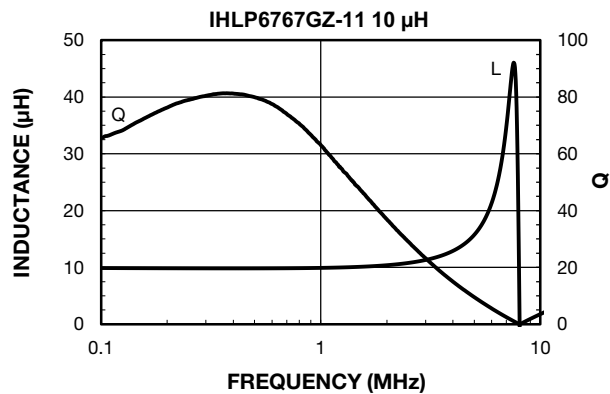
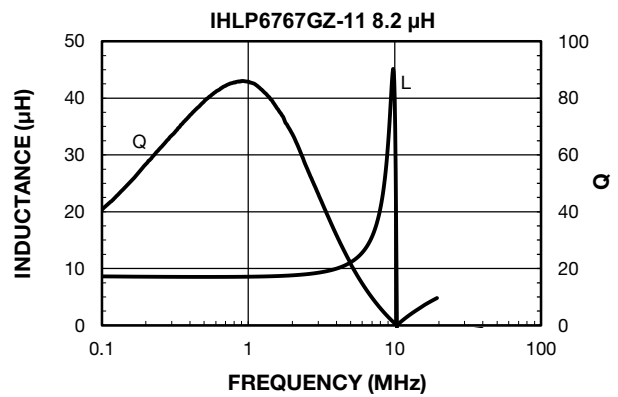
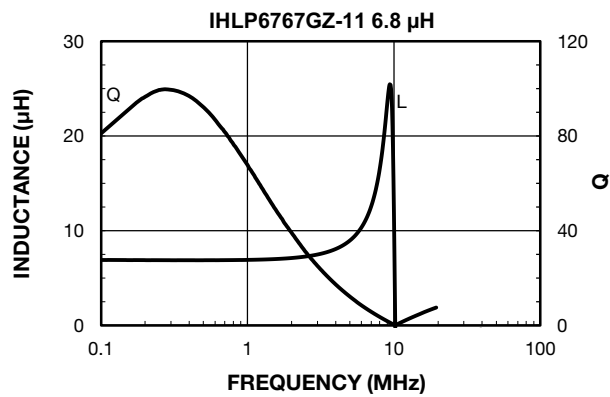
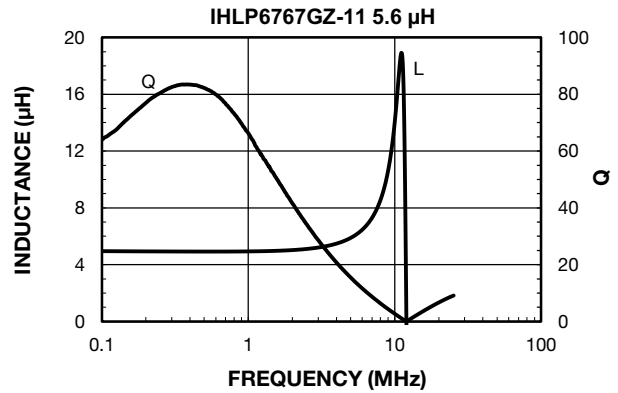
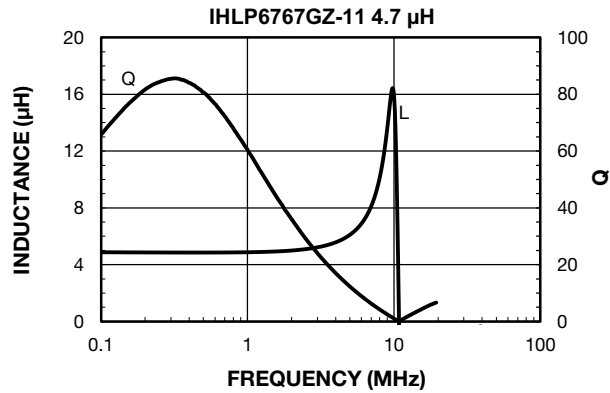


**PERFORMANCE GRAPHS: INDUCTANCE AND Q VS. FREQUENCY**



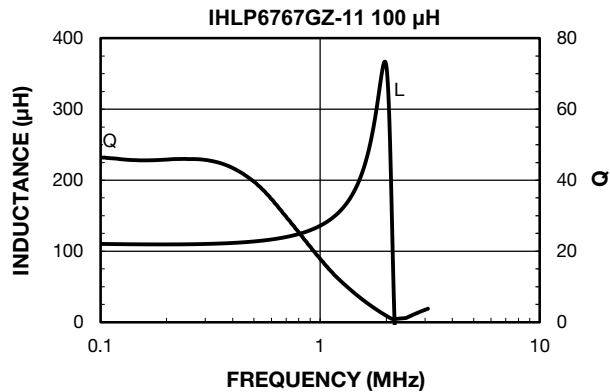
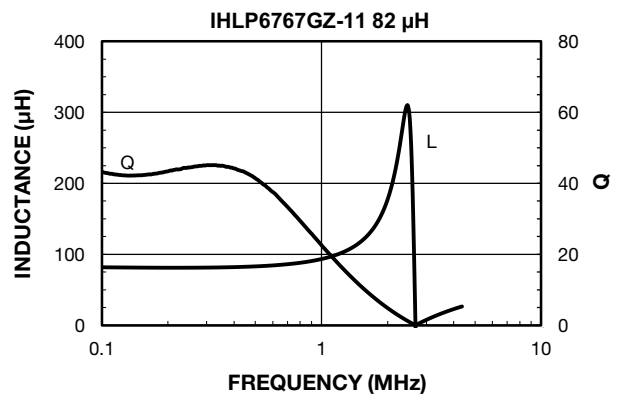
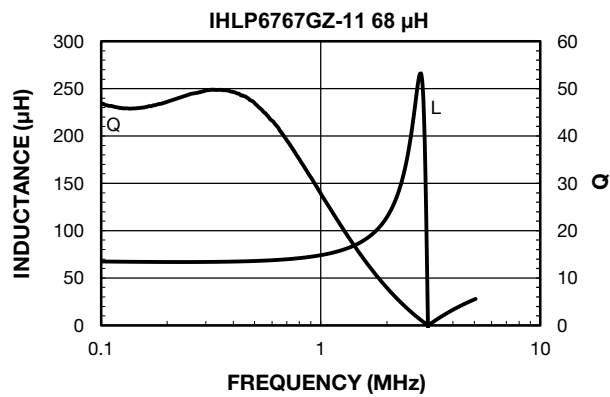
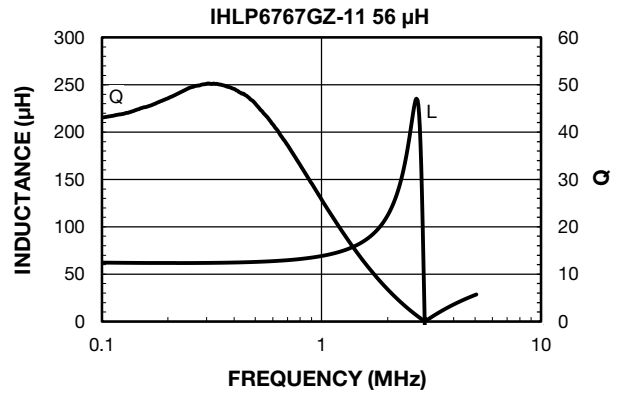
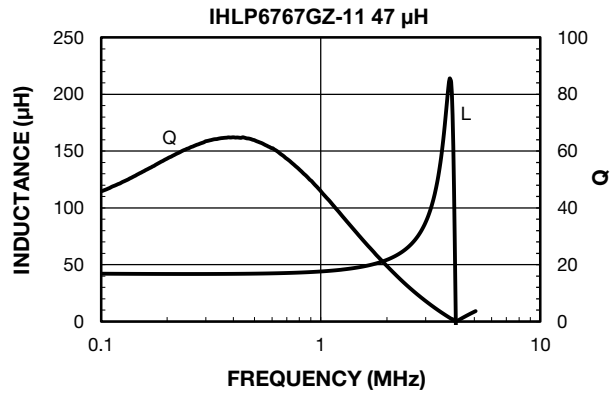


PERFORMANCE GRAPHS: INDUCTANCE AND Q VS. FREQUENCY





PERFORMANCE GRAPHS: INDUCTANCE AND Q VS. FREQUENCY





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