



A Product Line of Diodes Incorporated



ZXMP6A17E6

60V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)} Max	I _D Max T _A = 25°C (Note 5)
60)/	125mΩ @ V_{GS} = -10V	-3.0 A
-60V	190mΩ @ V_{GS} = -4.5V	-2.4 A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power management functions
- Disconnect switches
- Motor control



- Low on-resistance
- Fast switching speed
- Low threshold
- Low gate drive
- Low input capacitance
- "Lead Free", RoHS Compliant (Note 1)
- Halogen and Antimony Free. "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

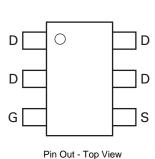
Mechanical Data

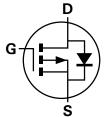
- Case: SOT-26
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Weight: 0.018 grams (approximate)



SOT-26

Top View





Equivalent Circuit

Ordering Information (Note 3)

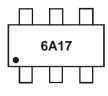
Product Marking Reel size (inches) Tape width (mm) Quantity per reel ZXMP6A17E6TA 6A17 7 8 3.000					
ZXMP6A17E6TA 6A17 7 8 3.000	Product	Marking	Reel size (inches)	Tapo width (mm)	Quantity per reel
	ZXMP6A17E6TA	6A17	7	8	3,000

Notes: 1. No purposefully added lead

2. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com.

3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



6A17 = Product Type Marking Code





Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit	
Drain-Source voltage			V _{DSS}	-60	V	
Gate-Source voltage			V _{GS}	±20	V	
		(Note 5)		-3.0		
Continuous Drain current	$V_{GS} = 10V$	$T_{A} = 70^{\circ}C$ (Note 5)	I _D	-2.4	A	
		(Note 4)		-2.3		
Pulsed Drain current	$V_{GS} = 10V$	(Note 6)	IDM	-13.6	А	
Continuous Source current (Body diode) (Note 5)		(Note 5)	Is	-2.5	А	
Pulsed Source current (Body diode) (Note 6)		(Note 6)	I _{SM}	-13.6	А	

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit	
Power dissipation	(Note 4)		1.1 8.8	W	
Linear derating factor	(Note 5)	PD	1.92 15.4	mW/°C	
Thermal Desistance Innetion to Ambient	(Note 4)		113	0000	
Thermal Resistance, Junction to Ambient	(Note 5)	R _{0JA}	65	°C/W	
Operating and storage temperature range		TJ, TSTG	-55 to 150	°C	

Notes: 4. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

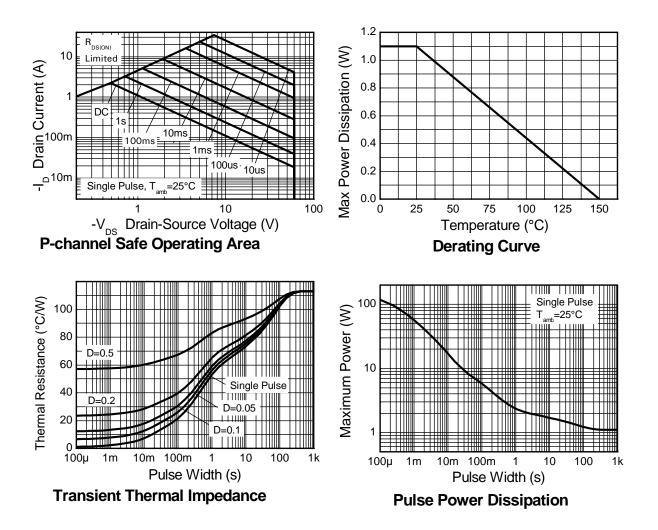
5. Same as note (4), except the device is measured at t \leq 5 sec.

6. Same as note (4), except the device is pulsed with D = 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature.





Thermal Characteristics





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Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	-60	—	—	V	$I_{D} = -250 \mu A, V_{GS} = 0V$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1.0	μΑ	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V _{GS(th)}	-1.0	_	-3.0	V	I _D = -250μA, V _I	os = V _G s
Statia Drain Source On Basistones (Note 7)			0.100	0.125	Ω	$V_{GS} = -10V, I_{D} =$	= -2.3A
Static Drain-Source On-Resistance (Note 7)	R _{DS (ON)}		0.130 0.190	12	V _{GS} = -4.5V, I _D = -1.9A		
Forward Transconductance (Notes 7 & 8)	g fs	_	4.7		S	V _{DS} = -15V, I _D = -2.3A	
Diode Forward Voltage (Note 7)	V _{SD}	_	-0.85	-0.95	V	$I_{S} = -2A, V_{GS} = 0V$	
Reverse recovery time (Note 8)	t _{rr}		25.1	_	ns		
Reverse recovery charge (Note 8)	Q _{rr}	_	27.2	_	nC		
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	637		pF	$V_{DS} = -30V, V_{GS} = 0V$ f = 1MHz	
Output Capacitance	C _{oss}	_	70		pF		
Reverse Transfer Capacitance	Crss	_	53	—	pF		
Total Gate Charge (Note 9)	Qg	_	9.8		nC	$V_{GS} = -5.0V$	
Total Gate Charge (Note 9)	Qq	_	17.7		nC	V _{DS} = -3	
Gate-Source Charge (Note 9)	Q _{qs}	_	1.6		nC	V _{GS} = -10V	$I_{D} = -2.3A$
Gate-Drain Charge (Note 9)	Q _{gd}	_	4.4		nC]	
Turn-On Delay Time (Note 9)	t _{D(on)}	_	2.6		ns		
Turn-On Rise Time (Note 9)	tr	_	3.4	_	ns	$V_{DD} = -30V, V_{GS} = -10V$ $I_D = -1A, R_G \cong 6.0\Omega$	
Turn-Off Delay Time (Note 9)	t _{D(off)}	_	26.2	_	ns		
Turn-Off Fall Time (Note 9)	t _f		11.3		ns		

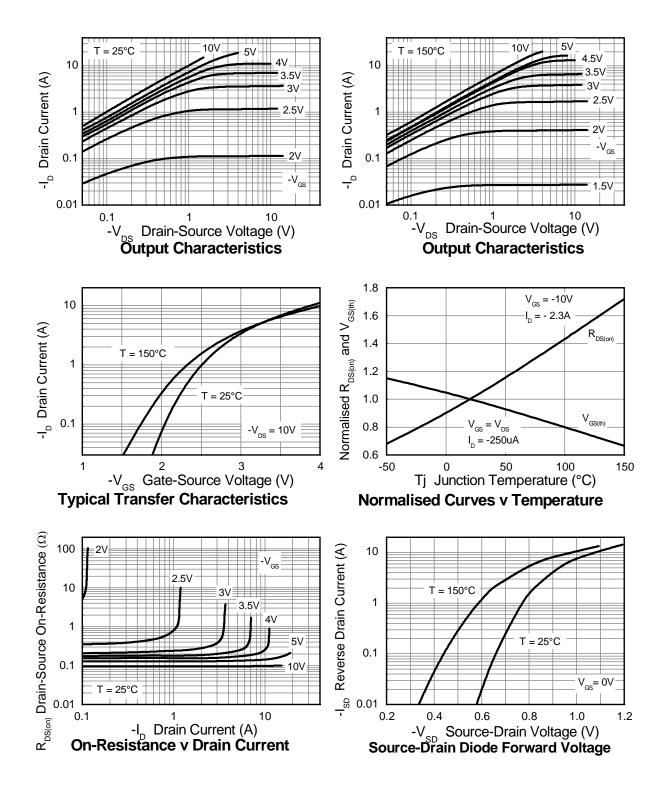
Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%
For design aid only, not subject to production testing.
Switching characteristics are independent of operating junction temperatures.

Notes:





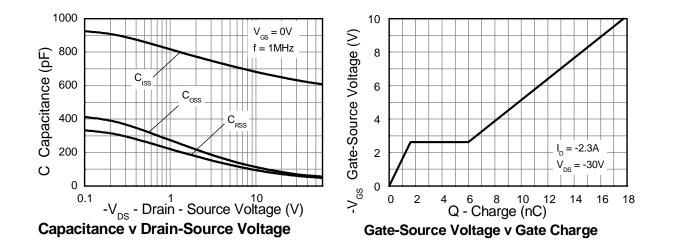
Typical Characteristics



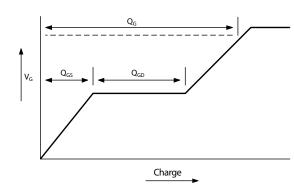




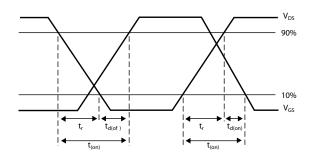
Typical Characteristics - continued



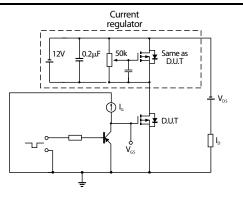
Test Circuits



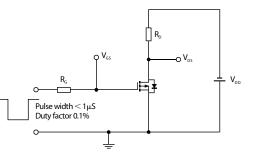
Basic gate charge waveform



Switching time waveforms

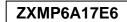


Gate charge test circuit

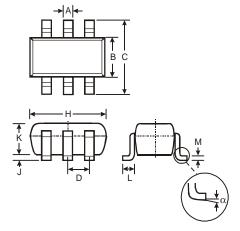


Switching time test circuit



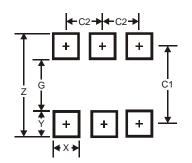


Package Outline Dimensions



SOT-26					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D	_		0.95		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
Κ	1.00	1.30	1.10		
L	0.35	0.55	0.40		
М	0.10	0.20	0.15		
α	0°	8°	_		
All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95



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