

Data Sheet

November 2013

# 60 A, 400 V - 600 V, Hyperfast Dual Diode

## **Description**

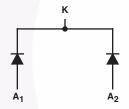
The RHRG3040CC, RHRG3060CC is a hyperfast dual diode with soft recovery characteristics. It has the half recovery time of ultrafast diodes and is silicon nitride passivated ionimplanted epitaxial planar construction. These devices are intended to be used as freewheeling/ clamping diodes and diodes in a variety of switching power supplies and other power switching applications. Their low stored charge and hyperfast soft recovery minimize ringing and electrical noise in many power switching circuits reducing power loss in the switching transistors.

### **Ordering Information**

PART NUMBER	PACKAGE	BRAND
RHRG3040CC	TO-247	RHRG3040C
RHRG3060CC	TO-247	RHRG3060C

NOTE: When ordering, use the entire part number.

# Symbol



#### **Features**

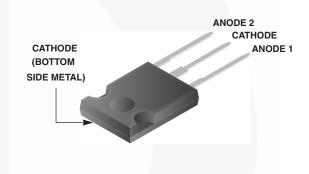
- Hyperfast Recovery  $t_{rr}$  = 45 ns (@  $I_F$  = 30 A)
- Max Forward Voltage, V<sub>F</sub> = 2.1 V (@ T<sub>C</sub> = 25°C)
- · High Reverse Voltage and High Reliability
- Avalanche Energy Rated
- RoHS Compliant

### **Applications**

- Switching Power Supplies
- Power Switching Circuits
- General Purpose

### Packaging

**JEDEC STYLE TO-247** 



#### **Absolute Maximum Rating** (Per Leg) T<sub>C</sub> = 25°C, Unless Otherwise Specified

	RHRG3040CC	RHRG3060CC	UNIT
Peak Repetitive Reverse Voltage	400	600	V
Working Peak Reverse Voltage	400	600	V
DC Blocking VoltageV <sub>R</sub>	400	600	V
Average Rectified Forward Current $I_{F(AV)}$ ( $T_C = 120^{\circ}C$ )	30	30	Α
Repetitive Peak Surge CurrentIFRM (Square Wave, 20 kHz)	70	70	Α
Nonrepetitive Peak Surge Current	325	325	Α
Maximum Power Dissipation	125	125	W
Avalanche Energy (See Figures 10 and 11)	20	20	mJ
Operating and Storage Temperature	-65 to 175	-65 to 175	οС

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 $\textbf{Electrical Specification} \; (\text{Per Leg}) \; \text{T}_{C} = 25^{\text{O}}\text{C}, \; \text{Unless Otherwise Specified}$ 

		RHRG3040CC		С	RHRG3060CC			
SYMBOL	TEST CONDITION	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
V <sub>F</sub>	I <sub>F</sub> = 30 A	-	-	2.1	-	-	2.1	V
	$I_F = 30 \text{ A}, T_C = 150^{\circ}\text{C}$	-	-	1.7	-	-	1.7	V
I <sub>R</sub>	V <sub>R</sub> = 400 V	-	-	250	-	-	-	μА
	V <sub>R</sub> = 600 V	-	-	-	-	-	250	μА
	$V_R = 400 \text{ V}, T_C = 150^{\circ}\text{C}$	-	-	1.0	-	-	-	mA
	$V_R = 600 \text{ V}, T_C = 150^{\circ}\text{C}$	-	-	-	-	-	1.0	mA
t <sub>rr</sub>	I <sub>F</sub> = 1 A, dI <sub>F</sub> /dt = 200 A/μs	-	-	40	-	-	40	ns
	I <sub>F</sub> = 30 A, dI <sub>F</sub> /dt = 200 A/μs	-	-	45	-	-	45	ns
t <sub>a</sub>	$I_F = 30 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}$	-	22	-	-	22	-	ns
t <sub>b</sub>	I <sub>F</sub> = 30 A, dI <sub>F</sub> /dt = 200 A/μs	-	18	-	-	18	-	ns
Q <sub>rr</sub>	I <sub>F</sub> = 30 A, dI <sub>F</sub> /dt = 200 A/μs	-	100	-	-	100	-	nC
СЈ	V <sub>R</sub> = 10 V, I <sub>F</sub> = 0 A	-	85	-	-	85	-	pF
$R_{\theta JC}$		-	-	1.2	-	-	1.2	°C/W

#### **DEFINITIONS**

 $V_F$  = Instantaneous forward voltage (pw = 300  $\mu$ s, D = 2%).

I<sub>R</sub> = Instantaneous reverse current.

 $T_{rr}$  = Reverse recovery time (See Figure 9), summation of  $t_a + t_b$ .

ta = Time to reach peak reverse current (See Figure 9).

 $t_b$  = Time from peak  $I_{RM}$  to projected zero crossing of  $I_{RM}$  based on a straight line from peak  $I_{RM}$  through 25% of  $I_{RM}$  (See Figure 9).

Q<sub>rr</sub> = Reverse recovery charge.

C<sub>J</sub> = Junction Capacitance.

 $R_{\theta,JC}$  = Thermal resistance junction to case.

pw = pulse width.

D = duty cycle.

### **Typical Performance Curves**

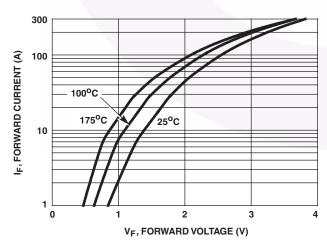


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

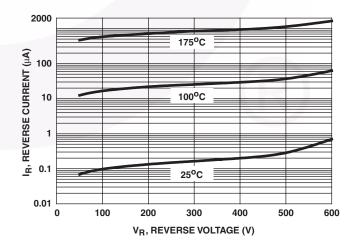


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

# Typical Performance Curves (Continued)

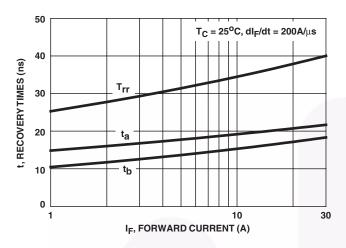


FIGURE 3. T<sub>rr</sub>, t<sub>a</sub> AND t<sub>b</sub> CURVES vs FORWARD CURRENT

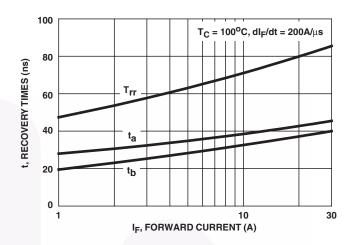


FIGURE 4. T<sub>rr</sub>, t<sub>a</sub> AND t<sub>b</sub> CURVES vs FORWARD CURRENT

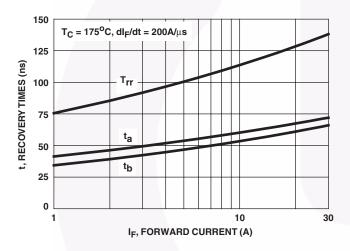


FIGURE 5. T<sub>rr</sub>, t<sub>a</sub> AND t<sub>b</sub> CURVES vs FORWARD CURRENT

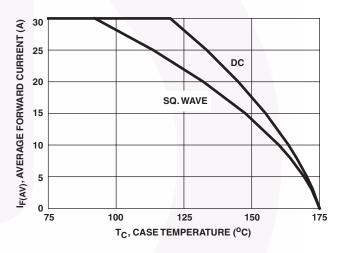


FIGURE 6. CURRENT DERATING CURVE

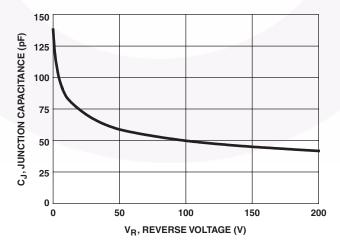


FIGURE 7. JUNCTION CAPACITANCE vs REVERSE VOLTAGE

#### Test Circuits and Waveforms

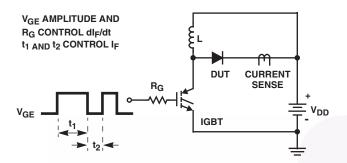
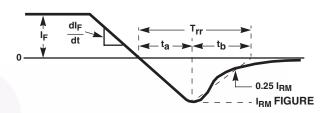


FIGURE 8. T<sub>rr</sub> TEST CIRCUIT

 $I_{MAX} = 1A$  L = 40mH  $R < 0.1\Omega$   $E_{AVL} = 1/2Li^2 \left[ V_{R(AVL)} / (V_{R(AVL)} - V_{DD}) \right]$   $Q_1 = IGBT \left( BV_{CES} > DUT V_{R(AVL)} \right)$  CURRENT + 0  $SENSE V_{DD}$   $V_{DD}$   $V_{DD}$ 

FIGURE 10. AVALANCHE ENERGY TEST CIRCUIT



9.  $T_{rr}$  WAVEFORMS AND DEFINITIONS

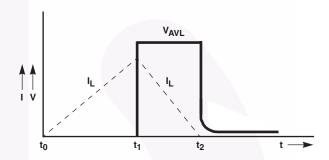
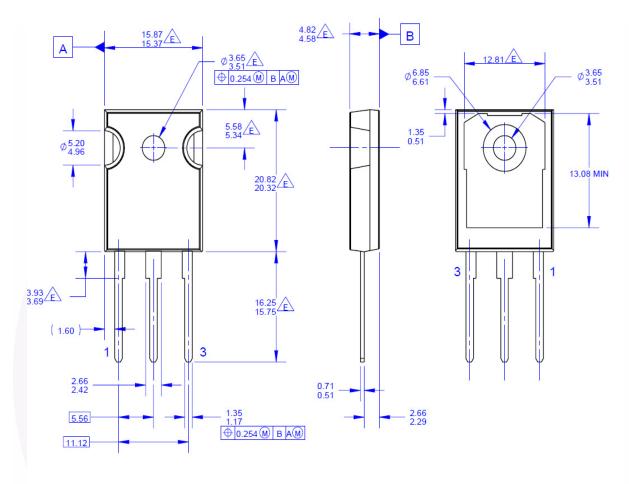


FIGURE 11. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

# TO247-3L



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004.
- DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- ALL DIMENSIONS ARE IN MILLIMETERS.
  DRAWING CONFORMS TO ASME Y14.5 1994

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Figure 12. TO-247, Molded, 3LD, Jedec Option AB

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Rev. 166