Schottky barrier single diodes Rev. 01 — 21 May 2007

Product data sheet

### 1. Product profile

### 1.1 General description

Planar Schottky barrier single diodes with an integrated guard ring for stress protection, encapsulated in small Surface-Mounted Device (SMD) plastic packages.

#### Table 1. **Product overview**

Type number	Package	Package		
	NXP	JEITA	configuration	
RB751CS40	SOD882	-	leadless ultra small	
RB751S40	SOD523	SC-79	ultra small	
RB751V40	SOD323	SC-76	very small	

#### **1.2 Features**

- Low forward voltage
- Low capacitance

#### **1.3 Applications**

- Ultra high-speed switching
- Voltage clamping
- Line termination
- Reverse polarity protection

### 1.4 Quick reference data

#### Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>F</sub>	forward current		-	-	120	mA
V <sub>RRM</sub>	repetitive peak reverse voltage		-	-	40	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 mA	<u>[1]</u> _	-	370	mV

[1] Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ .



Schottky barrier single diodes

## 2. Pinning information

Pin	Description	Simplified outline Symbol
SOD882		
1	cathode	[1]
2	anode	
		sym001
		Transparent top view
SOD323;	SOD523	
1	cathode	[1]
2	anode	
		1 2 sym001
		001aab540

[1] The marking bar indicates the cathode.

### 3. Ordering information

Type number	Package		
	Name	Description	Version
RB751CS40	-	leadless ultra small plastic package; 2 terminals; body $1.0 \times 0.6 \times 0.5$ mm	SOD882
RB751S40	SC-79	plastic surface-mounted package; 2 leads	SOD523
RB751V40	SC-76	plastic surface-mounted package; 2 leads	SOD323

### 4. Marking

#### Table 5. Marking codes

Type number	Marking code
RB751CS40	F6
RB751S40	G4
RB751V40	W8

### 5. Limiting values

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>RRM</sub>	repetitive peak reverse voltage		-	40	V
V <sub>R</sub>	reverse voltage		-	40	V
I <sub>F</sub>	forward current		-	120	mA
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; t <sub>p</sub> < 10 ms	-	200	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[1]</u>		
	RB751CS40		[2] _	250	mW
	RB751S40		[2] _	280	mW
	RB751V40		-	280	mW
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

### 6. Thermal characteristics

Thermal characteristics					
Parameter	Conditions	Min	Тур	Max	Unit
thermal resistance from junction to ambient	in free air	<u>[1]</u>			
RB751CS40		[2] _	-	500	K/W
RB751S40		[2] _	-	450	K/W
RB751V40		-	-	450	K/W
	Parameterthermal resistance from junction to ambientRB751CS40RB751S40	ParameterConditionsthermal resistance from junction to ambientin free airRB751CS40RB751S40	ParameterConditionsMinthermal resistance from junction to ambientin free air[1]RB751CS40[2]-RB751S40[2]-	ParameterConditionsMinTypthermal resistance from junction to ambientin free air[1]RB751CS40[2]RB751S40[2]	ParameterConditionsMinTypMaxthermal resistance from junction to ambientin free air[1]

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

### 7. Characteristics

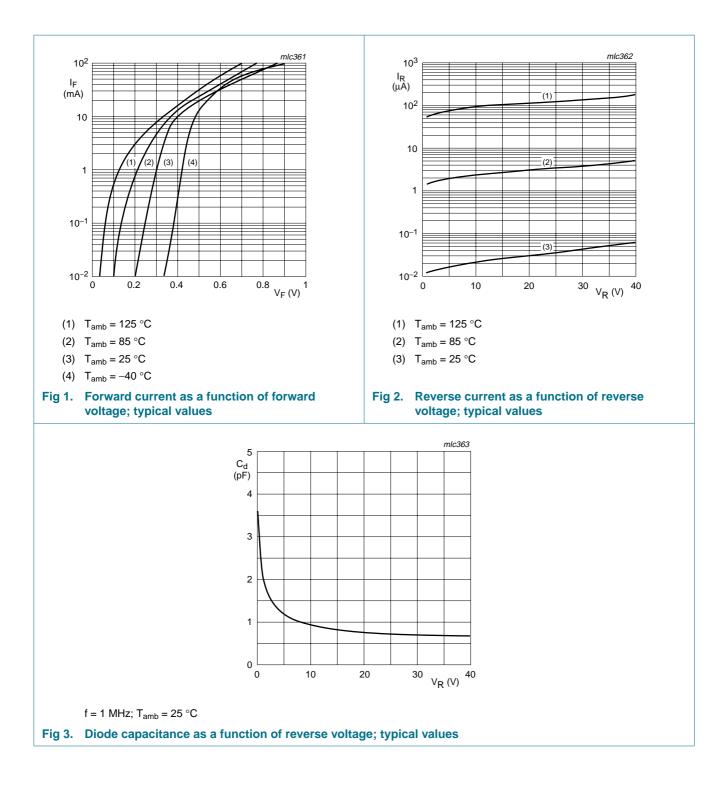
#### Table 8.Characteristics

 $T_{amb} = 25 \circ C$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 mA	<u>[1]</u> _	-	370	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 30 V	-	-	0.5	μΑ
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz	-	2	-	pF

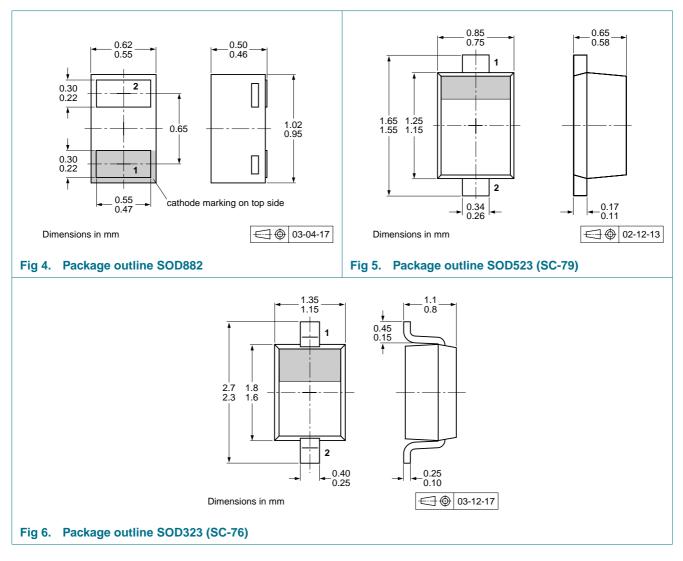
[1] Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ .

Schottky barrier single diodes



Schottky barrier single diodes

## 8. Package outline



### 9. Packing information

#### Table 9.Packing methods

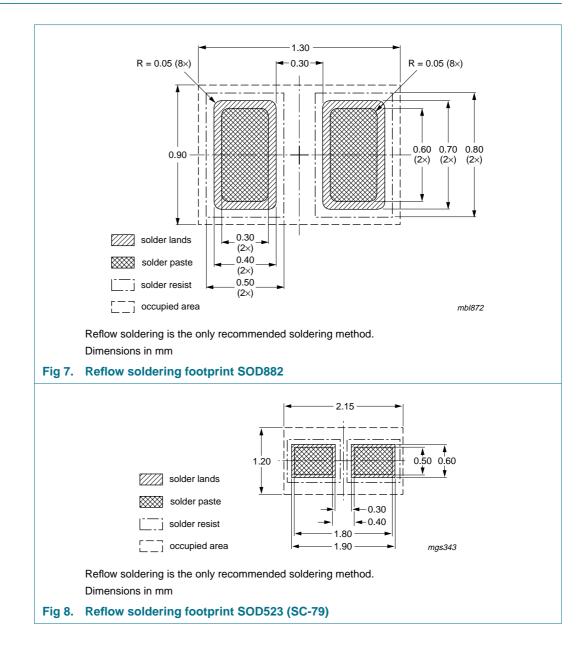
The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packin	Packing quantity		
			3000	8000	10000	
RB751CS40	SOD882	2 mm pitch, 8 mm tape and reel	-	-	-315	
RB751S40	RB751S40 SOD523	2 mm pitch, 8 mm tape and reel	-	-315	-	
		4 mm pitch, 8 mm tape and reel	-115	-	-135	
RB751V40	SOD323	4 mm pitch, 8 mm tape and reel	-115	-	-135	

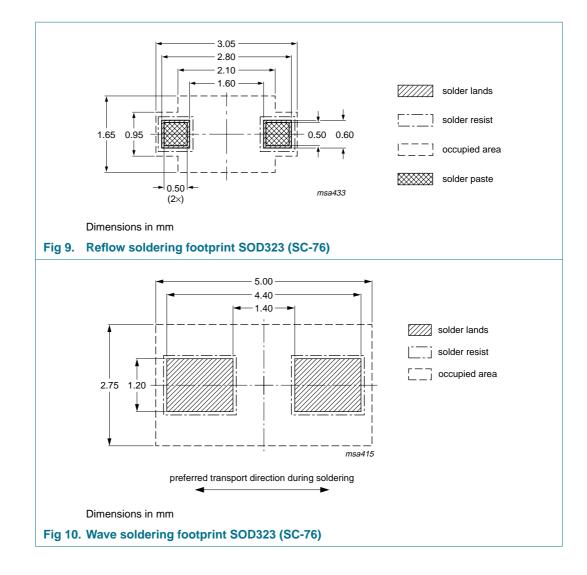
[1] For further information and the availability of packing methods, see <u>Section 13</u>.

Schottky barrier single diodes

### **10. Soldering**



#### Schottky barrier single diodes



RB751\_SER\_1 Product data sheet

Schottky barrier single diodes

## **11. Revision history**

Table 10. Revision hist	ory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
RB751_SER_1	20070521	Product data sheet	-	-

### **12. Legal information**

### 12.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

#### 12.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

#### 12.3 Disclaimers

**General** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or

malfunction of a NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <a href="http://www.nxp.com/profile/terms">http://www.nxp.com/profile/terms</a>, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

#### 12.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

### **13. Contact information**

For additional information, please visit: http://www.nxp.com

For sales office addresses, send an email to: salesaddresses@nxp.com

#### Schottky barrier single diodes

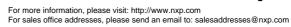
### **14. Contents**

1	Product profile 1
1.1	General description
1.2	Features
1.3	Applications 1
1.4	Quick reference data 1
2	Pinning information 2
3	Ordering information 2
4	Marking 2
5	Limiting values 3
6	Thermal characteristics 3
7	Characteristics 3
8	Package outline 5
9	Packing information 5
10	Soldering 6
11	Revision history 8
12	Legal information 9
12.1	Data sheet status 9
12.2	Definitions
12.3	Disclaimers
12.4	Trademarks
13	Contact information 9
14	Contents 10

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© NXP B.V. 2007.

All rights reserved.



Date of release: 21 May 2007 Document identifier: RB751\_SER\_1

