



### 120V NPN DARLINGTON TRANSISTOR IN SOT223

#### **Features**

- BV<sub>CEO</sub> > 120V
- BV<sub>CBO</sub> > 140V
- I<sub>C</sub> = 1.5A High Continuous current
- hFE > 2k for High Gain @ 1A
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

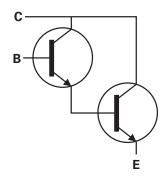
- Case: SOT223
- Case material: molded plastic. "Green" molding compound.
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (a)
- Weight: 0.112 grams (approximate)

## **Applications**

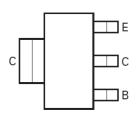
- Lamp
- Relay
- Solenoid Driving







Device Symbol



Top View Pin-Out

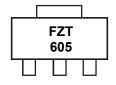
#### Ordering Information (Note 4)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
FZT605TA	FZT605	7	12	1,000
FZT605TC	FZT605	13	12	4,000

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

## **Marking Information**



FZT605= Product Type Marking Code



# **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	$V_{CBO}$	140	V
Collector-Emitter Voltage	$V_{CEO}$	120	V
Emitter-Base Voltage	$V_{EBO}$	14	V
Continuous Collector Current	lc	1.5	Α
Peak Pulse Current	I <sub>CM</sub>	4	Α

### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Power Dissipation	(Note 5)	0	2	W
Power Dissipation	(Note 6)	P <sub>D</sub>	3	W
Thermal Desistance Junction to Ambient	(Note 5)	-	62.5	°C/W
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	41.7	°C/W
Thermal Resistance, Junction to Leads (Note 7)		R <sub>0JL</sub>	12.93	°C/W
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to +150	°C	

## ESD Ratings (Note 8)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	≥ 400	V	С

Notes:

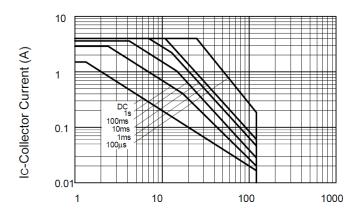
- 5. For a device mounted on 25mm X 25mm 1oz weight copper that is on a single-sided FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
- 6. Same as note (5), except the device is mounted on 50mm X 50mm 2oz copper.
- 7. Thermal resistance from junction to solder-point (at the end of the collector lead).

  8. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



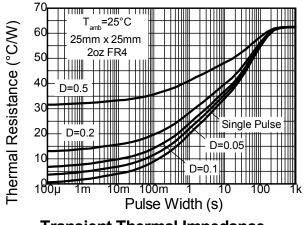


## **Thermal Characteristics and Derating Information**

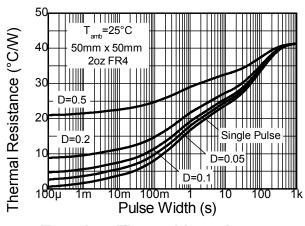


VCE - Collector Emitter Voltage (V)

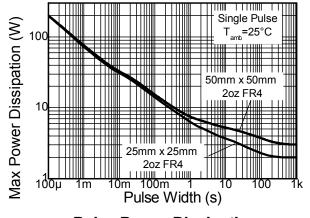
#### FZT605 Safe Operating Area



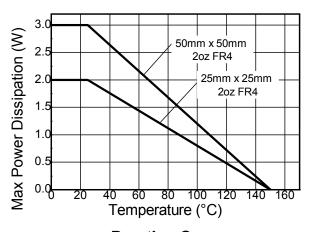
**Transient Thermal Impedance** 



**Transient Thermal Impedance** 



**Pulse Power Dissipation** 



**Derating Curve** 



## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

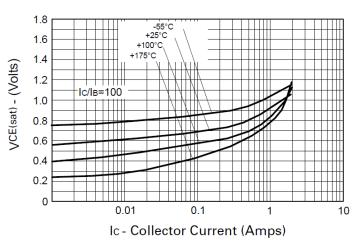
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	140	_	_	V	I <sub>C</sub> = 100μA
Collector-Emitter Breakdown Voltage (Note 9)	BV <sub>CEO</sub>	120	_	_	V	I <sub>C</sub> = 1mA
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	14	_	_	V	I <sub>E</sub> = 100μA
Collector-Base Cutoff Current	I <sub>CBO</sub>	-	_	100 10	nΑ μΑ	V <sub>CB</sub> = 120V V <sub>CB</sub> = 120V, T <sub>A</sub> = +120°C
Collector-Emitter Cutoff Current	I <sub>CES</sub>	-	_	100	nA	V <sub>CE</sub> = 120V
Emitter Cutoff Current	I <sub>EBO</sub>	-	_	100	nA	V <sub>EB</sub> = 8V
DC Current Gain (Note 9)	h <sub>FE</sub>	2,000 5,000 2,000 500	  -  -  -	_ _ 100,000 _	-	$I_{C}$ = 50mA, $V_{CE}$ = 5V $I_{C}$ = 500mA, $V_{CE}$ = 5V $I_{C}$ = 1A, $V_{CE}$ = 5V $I_{C}$ = 2A, $V_{CE}$ = 5V
Collector-Emitter Saturation Voltage (Note 9)	V <sub>CE(sat)</sub>	_	_	1 1.5	V	I <sub>C</sub> = 250mA, I <sub>B</sub> = 0.25mA I <sub>C</sub> = 1A, I <sub>B</sub> = 1mA
Base-Emitter Saturation Voltage (Note 9)	V <sub>BE(sat)</sub>	_	_	1.8	V	I <sub>C</sub> = 1A, I <sub>B</sub> = 1mA
Base-Emitter Turn-On Voltage (Note 9)	V <sub>BE(on)</sub>	_	_	1.7	V	I <sub>C</sub> = 1A, V <sub>CE</sub> = 5V
Input Capacitance (Note 9)	C <sub>ibo</sub>	_	90	_	pF	V <sub>EB</sub> = 0.5V, f = 1MHz
Output Capacitance (Note 9)	C <sub>obo</sub>	_	15	_	pF	V <sub>CB</sub> = 10V, f = 1MHz
Current Gain-Bandwidth Product (Note 9)	f <sub>T</sub>	150	_	_	MHz	V <sub>CE</sub> = 10V, I <sub>C</sub> = 100mA, f=20MHz
Turn-On Time	t <sub>on</sub>	_	0.5	_	μs	V <sub>CC</sub> = 10V, I <sub>C</sub> = 500mA
Turn-Off Time	t <sub>off</sub>		1.6		μs	$I_{B1} = -I_{B2} = 0.5 \text{mA}$

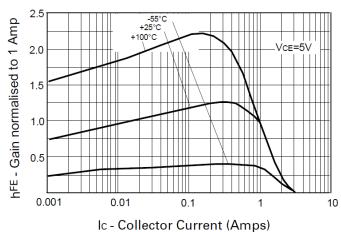
Note: 9. Measured under pulsed conditions. Pulse width  $\leq$  300  $\mu$ s. Duty cycle  $\leq$  2%.





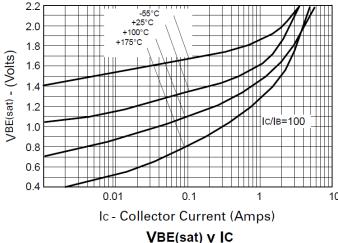
## Typical Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

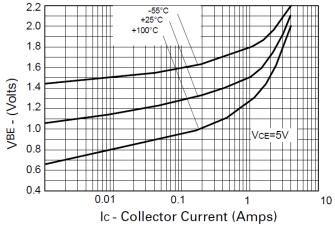




VCE(sat) v IC





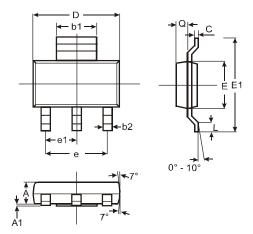


VBE(on) v IC



## **Package Outline Dimensions**

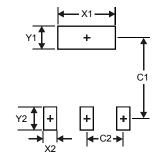
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b1	2.90	3.10	3.00		
b2	0.60	0.80	0.70		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Е	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	_	_	4.60		
e1	_	_	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
X1	3.3
X2	1.2
Y1	1.6
Y2	1.6
C1	6.4
C2	2.3

For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.





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