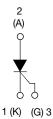


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Vishay Semiconductors

High Voltage Phase Control Thyristor, 40 A

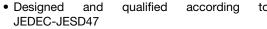




TO-247AC

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FEATURES





- Low I_{GT} parts available
- Compliant to RoHS Directive 2002/95/EC
- 125 °C max. operating junction temperature
- Halogen-free according to IEC 61249-2-21 definition (-M3 only)

HALOGEN FREE

APPLICATIONS

· Typical usage is in input rectification crowbar (soft start) and AC switch motor control, UPS, welding and battery charge

DESCRIPTION

The VS-40TPS... high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

PRODUCT SUMMARY					
TO-247AC					
Single SCR					
35 A					
800 V, 1200 V					
1.45 V					
150 mA					
- 40 °C to 125 °C					

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	35	А		
I _{RMS}		55	Α		
V _{RRM} /V _{DRM}		800/1200	V		
I _{TSM}		500	Α		
V _T	40 A, T _J = 25 °C	1.45	V		
dV/dt		1000	V/µs		
dl/dt		100	A/µs		
T _J		- 40 to 125	°C		

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA			
VS-40TPS08APbF, VS-40TPS08A-M3	800	900	10			
VS-40TPS08PbF, VS-40TPS08-M3	1200	1300				
VS-40TPS12APbF, VS-40TPS12A-M3	800	900				
VS-40TPS12PbF, VS-40TPS12-M3	1200	1300	1			



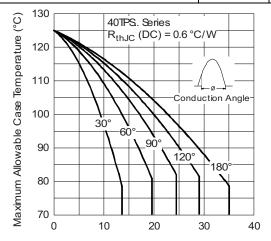
ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum average on-state current	I _{T(AV)}	T _C = 79 °C, 180° cc	T _C = 79 °C, 180° conduction half sine wave			
Maximum continuous RMS on-state current as AC switch	I _{T(RMS)}				55	А
Maximum peak, one-cycle	L	10 ms sine pulse, ra	ated V _{RRM} applied		500	
non-repetitive surge current	I _{TSM}	10 ms sine pulse, n	o voltage reapplied		600	
Maximum I ² t for fusing	l ² t	10 ms sine pulse, ra	ated V _{RRM} applied	Initial T _J = T _{.I} maximum	1250	A ² s
Maximum I-t for fusing	1-1	10 ms sine pulse, n	o voltage reapplied	TJTTIAXIITIAITI	1760	
Maximum l²√t for fusing	I²√t	t = 0.1 ms to 10 ms	, no voltage reapplied		12 500	A²√s
Low level value of threshold voltage	V _{T(TO)1}				1.02	V
High level value of threshold voltage	V _{T(TO)2}	T 105 °C	1.23	V		
Low level value of on-state slope resistance	r _{t1}	T _J = 125 °C			9.74	mΩ
High level value of on-state slope resistance	r _{t2}				7.50	
Maximum peak on-state voltage	V_{TM}	110 A, T _J = 25 °C			1.85	V
Maximum rate of rise of turned-on current	dl/dt	T _J = 25 °C			100	A/μs
Maximum holding current	I _H				150	
Maximum latching current	ΙL				300	
		$V_{R} = Rated V_{RRM}/V_{DRM}$ $V_{R} = Rated V_{RRM}/V_{DRM}$			0.5	mA
Maximum reverse and direct leakage current	I _{RRM/} I _{DRM}			10	-	
Maximum rate of rise of off-state voltage 40TPS08	ط/\//ط±	T. T. and its and its and all 200 MV. P. L. C.			500	1////
Maximum rate of rise of off-state voltage 40TPS12	dV/dt	$T_J = T_J$ maximum, linear to 80 % V_{DRM} , R_g -k = Open		V/μs 1000		

TRIGGERING					
PARAMETER	SYMBOL	TEST CO	ONDITIONS	VALUES	UNITS
Maximum peak gate power	P _{GM}			10	W
Maximum average gate power	P _{G(AV)}			2.5	VV
Maximum peak gate current	I _{GM}			2.5	Α
Maximum peak negative gate voltage	- V _{GM}			10	V
	V _{GT}	T _J = - 40 °C		4.0	V
Maximum required DC gate voltage to trigger		T _J = 25 °C	Anode supply = 6 V resistive load	2.5	
		T _J = 125 °C		1.7	
		T _J = - 40 °C		270	
Maximum vacuited DC acts assument to triangue		T _J = 25 °C		150	A
Maximum required DC gate current to trigger	I _{GT}	T _J = 125 °C		80	mA
		T _J = 25 °C, for 40TPS08APbF and 40TPS12APbF		40	
Maximum DC gate voltage not to trigger	V_{GD}	T _J = 125 °C, V _{DRM} = Rated value		0.25	V
Maximum DC gate current not to trigger	I _{GD}			6	mA



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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 40 to 125	°C	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.6		
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	40	°C/W	
Maximum thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.2		
Approximate weight			6	g	
Approximate weight			0.21	oz.	
Mounting to raise	n		6 (5)	kgf · cm	
Mounting torque maximum	n		12 (10)	(lbf · in)	
			40TP	S08A	
Marking device			40TPS12A		
		Case style TO-247AC	40TPS08		
			40TF	40TPS12	



Average On-state Current (A)
Fig. 1 - Current Rating Characteristics

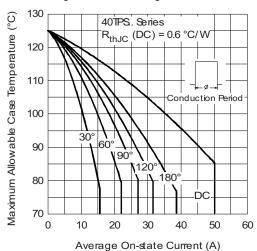


Fig. 2 - Current Rating Characteristics

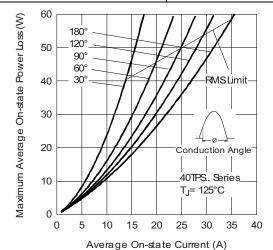
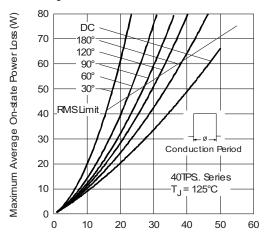
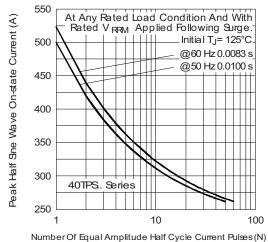


Fig. 3 - On-State Power Loss Characteristics



Average On-state Current (A)
Fig. 4 - On-State Power Loss Characteristics

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Fig. 5 - Maximum Non-Repetitive Surge Current

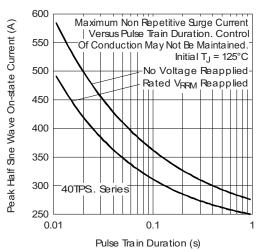


Fig. 6 - Maximum Non-Repetitive Surge Current

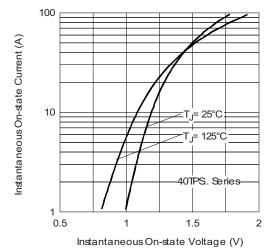
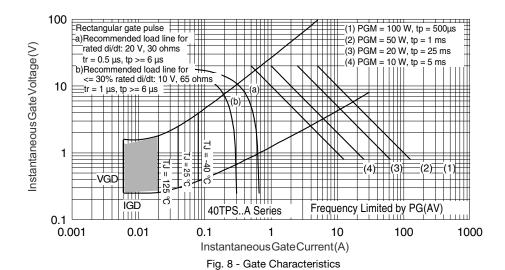


Fig. 7 - On-State Voltage Drop Characteristics



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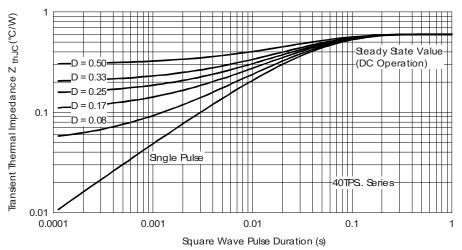
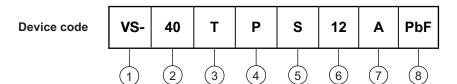


Fig. 9 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE



- Vishay Semiconductors product
- Current rating (40 = 40 A)
- Circuit configuration:

T = Thyristor

- Package:
 - P = TO-247
- 5 Type of silicon:
 - S = Standard recovery rectifier
- Voltage ratings • A = Low Igt selection 40 mA maximum
- None = Standard Igt selection
- 8 Environmental digit:

PbF = Lead (Pb)-free and RoHS compliant

-M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free

08 = 800 V

12 = 1200 V



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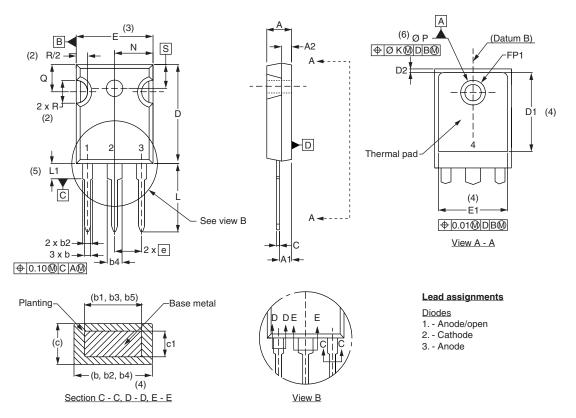
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-40TPS08APbF	25	500	Antistatic plastic tubes			
VS-40TPS08A-M3	25	500	Antistatic plastic tubes			
VS-40TPS08PbF	25	500	Antistatic plastic tubes			
VS-40TPS08-M3	25	500	Antistatic plastic tubes			
VS-40TPS12APbF	25	500	Antistatic plastic tubes			
VS-40TPS12A-M3	25	500	Antistatic plastic tubes			
VS-40TPS12PbF	25	500	Antistatic plastic tubes			
VS-40TPS12-M3	25	500	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS				
Dimensions		www.vishay.com/doc?95223		
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226		
Part marking information	TO-247AC-M3	www.vishay.com/doc?95007		



Vishay Semiconductors

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
FK	2.	54	0.0	010	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62	BSC	0	.3	
ΦР	3.56	3.66	0.14	0.144	
ФР1	1	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51	BSC	0.217	'BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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