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SPECIFICATION

PRODUCTS: ANTI-SURGE CHIP RESISTORS

TYPE: ESR03 SERIES

ROHM CO., LTD. RESISTOR DIV.

DESIGN Tadachi.	CHECK	APPROVAL	DATE: 26/JAN/2009	SPECIFICATION No.: ESRO3-IA
J. adadre	T. Hashiguani	A. Limmer	REV. 004E	ROHM Co.,Ltd.

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< Specifications (Precautions and Prohibitions) >

Safety Precautions

- 1) The products are designed and produced for application in ordinary electronic equipment (AV equipment, OA equipment, telecommunication equipment, home appliances, amusement equipment, etc.). If the products are to be used in devices requiring extremely high reliability (medical equipment, transport equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or operational error may endanger human life and sufficient fail-safe measures, please consult with the ROHM sales staff in advance. If product malfunctions may result in serious damage, including that to human life, sufficient fail-safe
 - [a] Installation of protection circuits or other protective devices to improve system safety
 - [b] Installation of redundant circuits in the case of single-circuit failure

measures must be taken, including the following:

- 2) The products are designed for use in a standard environment and not in any special environments. Application of the products in a special environment can deteriorate product performance. Accordingly, verification and confirmation of product performance, prior to use, is recommended if used under the following conditions:
 - [a] Use in various types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use outdoors where the products are exposed to direct sunlight, or in dusty places
 - [c] Use in places where the products are exposed to sea winds or corrosive gases, including Cl_2 , H_2S , NH_3 , SO_2 , and NO_2
 - [d] Use in places where the products are exposed to static electricity or electromagnetic waves
 - [e] Use in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Use involving sealing or coating the products with resin or other coating materials
 - [g] Use involving unclean solder or use of water or water-soluble cleaning agents for cleaning after soldering
 - [h] Use of the products in places subject to dew condensation
- 3) The products are not radiation resistant.
- 4) Verification and confirmation of performance characteristics of product, after on-board mounting, is advised.
- 5)In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse)is Applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state Loading condition may negatively affect product performance and reliability.
- 6) De-rate Power Dissipation(Pd)depending on Ambient temperature(Ta).
- 7) Confirm that operation temperature is within the specified range described in product specification.
- 8) Product may be damaged when the impact, such as downfall is given.
- 9) Failure induced under deviant condition from what defined in the product specification can be not be Guaranteed.
- 10) When product safety related problems arises, please immediately inform to ROHM, and consider technical counter measure.

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Precaution for Mounting/Circuit board design

- 1) When a highly active halogenous (chlorine, bromine, etc.)flux is used, the remainder of flux may negatively affect product performance and reliability.
- 2)In principle, the reflow soldering method must be used; if flow soldering method is preferred, please Consult with the company in advance.
- 3) Pay attention to the soldering condition in order to avoid problems due to silver absorption into solder.
- 4) Be careful when pick up the products with tweezers.

 There may be a case that the overcoat and /or the body can be chipped.
- 5) Soldering tip shall not touch the product when install product manually.

Precautions Regarding Application Examples and External Circuits

- 1) If change is made to the constant of an external circuit, allow a sufficient margin due to variations of the characteristics of the products and external components, including transient characteristics, as well as static characteristics.
- 2) The application examples, their constants, and other types of information contained herein are applicable only when the products are used in accordance with standard methods. Therefore, if mass production is intended, sufficient consideration to external conditions must be made.

Precaution for Electrostatic

This product is Electrostatic sensitive product, which may be damaged due to Electrostatic discharge. Please take proper caution during manufacturing and string so that voltage exceeding Product maximum rating won't be applied to products. Please take special care under dry condition(e.g. Grounding of human body /equipment /solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature /humidity control).

Precaution for strage/Transportation

- 1)Product performance and soldered connections may deteriorate if the products are stored in the following places:
 - [a] Where the products are exposed to sea winds or corrosive gases, including Cl_2 , H_2S , NH_3 , SO_2 , and NO_2
 - [b] Where the temperature or humidity exceeds those recommended by the Company Temperature:5—40°C, Humidity 30—80% (Put condition for individual product)
 - [c] Storage in direct sunshine or condensation
 - [d] Storage in high Electrostatic
- 2) Even under ROHM recommended storage condition, solderability of products over 1 year old (Put condition for each product)may be degraded.

It is strongly recommended storage time period.

- Recommended storage condition : Temperature 5—40°C, Humidity 30—80%(Put condition for individual product)
- 3) Store / transport cartons in the correct direction, which is indicated on a carton as a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton..

Precaution for product label

QR code printed on ROHM product label is only for internal use, and please do not use at customer site. It might contain a internal part number that is inconsistent with an product part number.

Precaution for disposition

When disposing products please dispose them properly with a industry waste company.

Precautions for Foreign exchange control regulation

ROHM has not determined whether or not the products are considered "a controlled product or labor" as specified in the Foreign Exchange and Foreign Trade Control Law.

Accordingly, if exportation of the products, either separately or integrated in another company's products, is intended, or giving the products to persons who are not residents is planed, additional steps are required, based upon the appropriate regulations.

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PRODUCTS
ANTI-SURGE CHIP RESISTORS

TYPE

ESR03 Series

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< Specifications (Precautions and Prohibitions) >

Prohibitions Regarding Industrial Property

- 1) These Specifications contain information related to the ROHM industrial property. Any use of them other than pertaining to the usage of appropriate products is not permitted. Duplication of these Specifications and its disclosure to a third party without the Company's permission is prohibited.
- 2) Information and data on products, including application examples, contained in these specifications are simply for reference; the Company does not guarantee any industrial property rights, intellectual property rights, or any other rights of a third party regarding this information or data. Accordingly, the Company does not bear any responsibility for:
 - [a] infringement of the intellectual property rights of a third party
 - [b] any problems incurred by the use of the products listed herein.
- 3) The Company prohibits the purchaser of its products to exercise or use the intellectual property rights, industrial property rights, or any other rights that either belong to or are controlled by the Company, other than the right to use, sell, or dispose of the products.

Other Matters

- Please sign these Specifications and return one copy to the Company.
 If a copy is not returned within three months after the issued date specified on the front page of these Specifications, the Company will consider the Specifications accepted.
- 2) If any matter related to these Specifications needs to be clarified, discussions shall be held promptly between the two parties concerned to determine the issue.

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PRODUCTS
ANTI-SURGE CHIP RESISTORS

TYPE ESRO3 Series

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1.SCOPE

This specification covers the characteristics of "ESR03 series" based of anti-surge chip resistors in R OHM Co., Ltd. products.

2.CLASSIFICATION



	,			V	
PACKAGING	PACKAGE	QUANTITY		TOLERANCE	,
CODE			$D(\pm 0.5\%)$	F (±1%)	J (±5%)
EZP	180mm(7inch) reel, paper tape (4mm pitch)	5,000pcs/reel	D	F	J

RESISTANCE VALUE

4 digits	D, F
3 digits	J

3.RATING

ITEMS	CONDITIONS	SPECIFICATIONS			
RATED	For resistors operated at the ambient temperature in	0.20W(1/5W)			
POWER	excess of 70°C, the load shall be derated in accordance with Fig.1	at 70°C			
	Fig.1 100 80 60 40 20 (%) -55 0 70 155 Ambient temperature (°C)				
RATED	Rated voltage is determined from the following.				
VOLTAGE	When rated voltage exceeds the limiting element voltage,				
	the limiting element voltage shall be the rated voltage.				
	$E = \sqrt{P \times R}$ E: RATED VOLTAGE (V)				
	P: RATED POWER (W)				
		VOLTACE 50V			
	R: RESISTANCE (Ω) LIMITING ELEMENT	VOLTAGE 50V			
RESISTANCE	See <u>Table.1</u>				
TEMPERATURE		-55°C ~ +155°C			

Table.1

TOLERANCE	(0)	TMPERATURE(ppm/°C)
	(Ω)	COEFFICIENT
D(±0.5%)	$10 \le R \le 1M \text{ (E24,96)}$	±100
F(±1%)	1≦R<10 (E24,96)	±200
	10≦R≦10M (E24,96)	±100
J(±5%)	$1 \le R \le 10M \text{ (E24)}$	±200

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ANTI-SURGE CHIP RESISTORS

TYPE ESRO3 Series

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4.CHARACTERISTICS	CHARANTEEDVALLE	TECT COMPITIONS (HS C 5201 1)
ITEMS	GUARANTEED VALUE	TEST CONDITIONS (JIS C 5201-1)
	RESISTOR TYPE	
4.1 RESISTANCE	D: $\pm 0.5\%$	JIS C 5201-1 4.5
	F: ±1%	
	J: ±5%	
4.2 VARIATION OF	See <u>Table.1</u>	JIS C 5201-1 4.8
RESISTANCE WITH		Measurement : -55/+25/+125°C
TEMPERATURE		Mounting condition: See Fig.3
4.3 OVERLOAD	$\pm (2.0\% + 0.1 \Omega)$	ЛS C 5201-1 4.13
		Rated voltage(current) \times 2.5, 2s
		Max.overload voltage: 100 V
		Mounting condition: See Fig.3
4.4 SOLDERABILITY	A new uniform coating of minimum of	JIS C 5201-1 4.17
	95% of the surface being immersed	Rosin · Ethanol(25%WT)
	and no soldering damage.	Soldering condition : 235±5℃
		Duration of immersion : 2.0 ± 0.5 s.
4.5 RESISTANCE TO	$\pm (1.0\% + 0.05 \Omega)$	JIS C 5201-1 4.18
SOLDERING HEAT	No remarkable abnormality on the	Soldering condition: 260±5°C
	appearance.	Duration of immersion: $10\pm1s$.
4.6 RAPID CHANGE OF	$\pm (1.0\% + 0.05 \Omega)$	JIS C 5201-1 4.19
TEMPERATURE		Test temp. : -55° C \sim +125 $^{\circ}$ C 5cyc
12.11 2.11 0.11		Mounting condition: See Fig.3
4.7 DAMP HEAT,	$\pm (3.0\% + 0.1 \Omega)$	JIS C 5201-1 4.24
4.7 DAMP HEAT, STEADY STATE	$\pm (3.070 + 0.152)$	40°C, 93%RH
STEADY STATE		Test time: $1,000h \sim 1,048h$
4.8 ENDURANCE	±(3.0%+0.1Ω)	Mounting condition: See Fig.3
AT 70°C	$\pm (3.0\% \pm 0.122)$	JIS C 5201-1 4.25.1
711 70 C		Rated voltage(current),70°C
		1.5h:ON=0.5h:OFF
		Test time: 1,000h~1,048h
4.0 50 50 50 50 50 50 50 50 50 50 50 50 50	1 (2.00/ 1.0.1.0)	Mounting condition: See Fig.3
4.9 ENDURANCE	$\pm (3.0\% + 0.1 \Omega)$	JIS C 5201 -1 4.25.3 155°C
		Test time: 1,000h~1,048h
		Mounting condition: See Fig.3
4.10 DESIGNANCE TO	$\pm (1.0\% + 0.05 \Omega)$	JIS C 5201-1 4.29
4.10 RESISTANCE TO	$\pm (1.070 + 0.0322)$	23 ± 5 °C, Immersion cleaning, 5 ± 0.5 min
SOLVENT		Solvent: 2-propanol
4.11 BEND STRENGTH	$\pm (1.0\% + 0.05 \Omega)$	JIS C 5201-1 4.33
OF THE END	Without mechanical damage such	Mounting condition: See Fig.4
FACE PLATING	as breaks.	
4.12 STATIC ELECTRIC	$\pm (5.0\% + 0.05 \Omega)$	EIAJ ED-4701 1300 TEST METHOD304
	$=(3.070\pm0.0352)$	VOLTAGE:3KV
CHARACTERISTICS		VOLTAGE:3KV R:1.5KΩ
		C:100pF
		APPLY CYCLE:1TIME

^{*} In the items on characteristics, the expression " $\pm (1.0\% + 0.05\,\Omega)$ " is used in the column for standard values.

However, this is because of dramatic increase in the fluctuation ratio that can be take place in the low resistance value range and is not meant to supplement the measuring accuracy of the measuring instruments.

Accordingly, there is a need to increase the design tolerance in the low resistance value range.

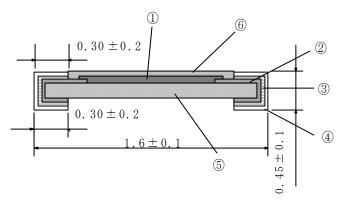
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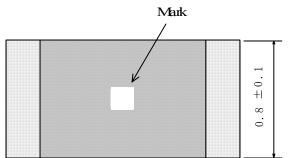
5. DIMENSIONS & CONSTRUCTION

Fig.2

(UNIT: mm)

ESR03 Series





No.	MATERIAL
1	Resistive element
2	Silver thick film electrode
3	Nickel electrode
4	Sn electrode
5	Alumina substrate
6	Over coating (Resin)

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ESR03 Series

6. Marking on the packaging container

The following items will be displayed on the smallest unit of the container used for Packaging.



- ① Type + Packaging code + Tolerance + Resistance value
- ② Bar code of type code + Resistance value
- Special code + Quantity + Lot No.(There may be label with and without special code.)
- 4 Bar code of Quantity + Lot No.
- ⑤ Code for ROHM internal use (This code is not always same as ①)
- Part No. + Order No. (To be executed on necessity)
- 7) The country of origin.
- QR code (Only for ROHM internal use)

7. APPEARANCE QUALITY

An appearance inspection of the surface should reveal no obvious abnormalities.

- ① There should be no obvious abnormalities such as bubbles, pin holes or cracks on the overcoat or outer termination.
- ② There should be no obvious electrode material or other foreign matter on the overcoat.
- 3 There should be no obvious electrode material or other foreign matter on back surface of the substrate and on side surface of the longitudinal axis.

8. MASS

The mass of the chip resistor is $2.0 \text{mg} \pm 0.5 \text{mg}$.

9. Deciphering the manufacturing date from the Lot No.

An example of the Lot No. is shown below. Read the manufacturing date and take first-in first-out method.

Example: 09 36 $\times \times \times \times \times$ 3

- ① Manufacturing year: Last two digits of the western calendar year. (2009)
- ② Week of manufacture: Shows week 01 to 53 in a year. (36:8/30 to 9/5)
- ③ Shows line number, serial number or manufacturing plant Code.

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SPECIFICATION No. : E S R O 3 - I A

1 0. RECOMMENDABLE CONDITION OF SOLDERING

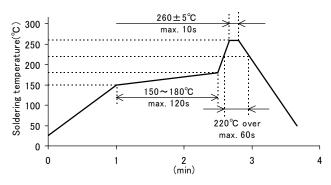
10. 1 Reflow Soldering

Reflow soldering with lead free solder.

Condition of soldering : Preheat 150~180°C less than 120s.

: Reflow zone 220°C less than 60s.

Maximum temperature : $260\pm5^{\circ}$ C less than 10s. Time : Maximum of twice.



10. 2 Reworking of soldering

Reworking of soldering shall be limited to one time.

① Temperature of soldering tip: 350°C or less.

② Working time: Not longer than 3s.

③ Soldering iron : 20W or less.

1 1. OTHERS

1 1 . 1 In regard to the Export Trade Control Decree.

Resistor That Rohm Co., Ltd. sells is not an object of controlled goods

in Annex 1(Item 1~15) of Export Trade Control Order.

But it is an object of controlled goods in Annex 1(Item 16) of Export Trade Control Order. In case of export, please confirm if it applies to "objective" criteria or an "informed"(by MITT clause) on the basis of "catch-all" controls for Non-Proliferation of Weapons of Mass Destruction.

1 1. 2 On use of ozone layer destroying substances

No ozone layer destroying substances are used in our resistors.

1 1. 3 On use of fluorocarbons

No specific fluorocarbons or alternative fluorocarbons are used in the manufacture of our resistors.

1 1 . 4 Regarding specific bromine type fire resistant materials

None of the following specific bromine type materials are used in our resistors.

- ① PBB0s
- ② PBBs

1 1 . 5 Requests prior to changes

In the manufacture and utilization of the chip resistors there are conditions that develop which require materials or processes to be changed. In such cases, request will be made prior to clang to obtain approval.

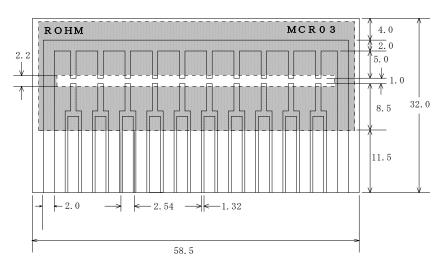
1 1 . 6 Manufacturing flow chart and basic design quality

As requested specially, the flow chart for the manufacturing processes of the chip resistor and the basic quality will be submitted separately.

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(UNIT: mm)

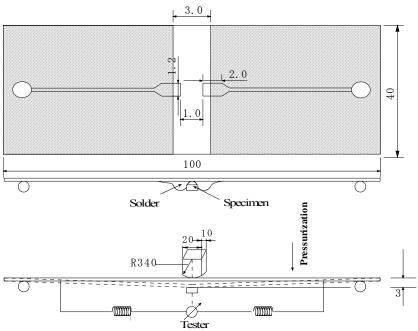
Fig.3: TEST BOARD A



NOTE) *1 The shaded area shows the solder resist treatment.

- *2 The terminals used for connectors are plated with rhodium.
- *3 All surface, except terminals used for connectors, receive pre-flux treatment.

Fig.4: TEST BOARD B



NOTE) *1 The shaded area shows the solder resist treatment.

- *2 All surface, except terminals used for connectors, receive pre-flux treatment.
- *3 During the test, the distance among support points shall be 90mm and the center of the chip resistor and the center between support points should be aligned to within ±2mm.

Laminate material: Glass fabric base epoxies.

Compatible with JIS C 6484

Thickness of board: 1.6mmt

Copper material: Copper purity is 99.5% or more.

Compatible with JIS C 6484 Copper foil thickness: 35µm

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ESR03 Series

1. SCOPE

This specification covers the tape package requirements for chip resistor ESR03, to be used on automatic placement systems.

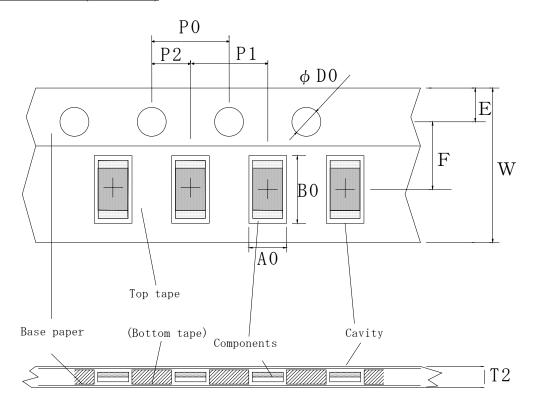
2. PACKAGING CODE

ESR03 EZP
TYPE PACKAGING
CODE

RESISTANCE

RESISTANCE RESISTANCE TOLERANCE VALUE (IEC CODE)

3. TAPE DIMENSION (UNIT: mm)



W	F	Е	A0	В0
8.0±0.3	3.5±0.05	1.75±0.1	1.1 ± 0.1	1.9±0.1
D0	P0	P1	P2	T2
+0.1 φ 1.5 0	4.0±0.1	4.0±0.1	2.0±0.05	MAX.1.1

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PRODUCTS

ANTI-SURGE CHIP RESISTORS

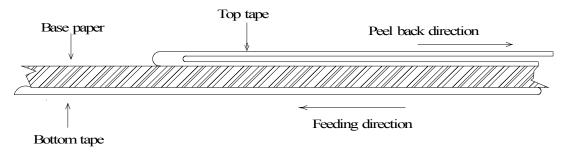
TYPE

ESR03 Series

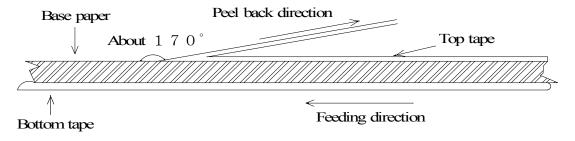
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4. MECHANICAL CHARACTERISTICS

4.1 COVER TAPE PEELING STRENGTH : $0.1N \leq PEELING STRENGTH \leq 0.6N$



4.2 Base tape should not adhere to top tape when top tape is peeled back, and peel back direction is as follows.



4.3 DURABILITY OF COVER TAPE (TOP TAPE)

Top tape shall not be off the base paper after 120hrs at the atmosphere of $60\pm3^{\circ}\text{C},90\sim95\%\text{R.H.}$

5. TAPE PACKAGING

- 5.1 Components are set in tape cavities with the same side (resistive paste upside).
- 5.2 The accumulated pitch tolerance shall be within ± 0.2 mm at 10 pitches.
- 5.3 Tape bent resistance

No damage on the tape and the cavity when tape is bent with the radius of 15mm.

- 5.4 Components in tape cavity shall not adhere to bottom / cover tape.
- 5.5 Components shall not be blocked by tape fragments or foreign materials when they are taken out from cavities.
- 5.6 The top tape shall not cover up the sprocket holes of tape.
- 5.7 The number of missing components shall not exceed 0.1% of the total number of components (marked number) or one whichever is the larger, and no consecutive missing exceeding two is allowed.

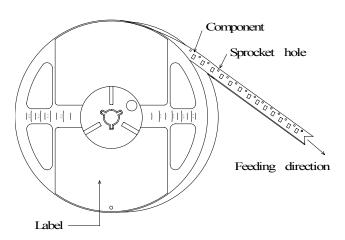
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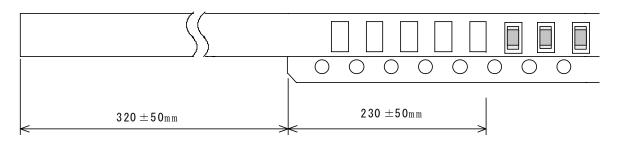
6. TAPE REEL

6.1 Tape feeding direction shall be shown in the picture drawn below.



6.2 Leader tape

Leader tape is given a portion of only cover tape and of blank cavities. (no resistor.)

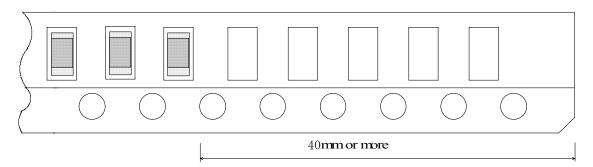


(Note) The leader portion of cover tape will not stick to embossed tape. (about 50~100mm)

6.3 Tail tape(trail tape)

Trail tape is given a portion of blank cavities (no resistor).

And the trail tape should not be fixed by adhesive to real and must be the one which can be pulled out easily from the reel.



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PRODUCTS

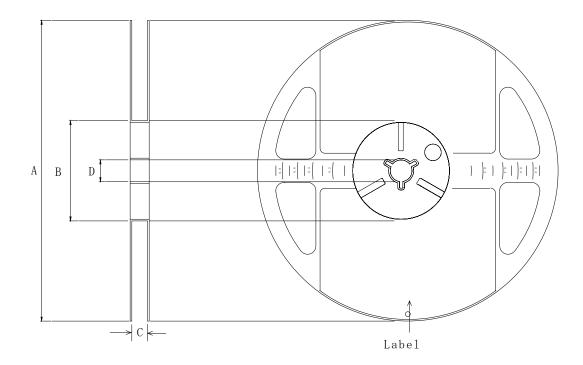
ANTI-SURGE CHIP RESISTORS

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7. REEL DIMENSIONS (UNIT: mm)



ACCORDING TO EIAJ ET-7200B

A	В	С	D
0	+1	+1.0	
φ 180	ϕ 60	9	$\phi 13 \pm 0.2$
-1.5	0	0	

 $\underline{MATERIAL}$

REEL: POLYSTYRENE

PACKING

5,000pcs / Reel

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